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Altium adds Aldec FPGA simulation technology to Altium Designer

Sydney, Australia – May 26, 2010 – Altium and Aldec have signed an OEM agreement that adds Aldec's FPGA simulation capabilities to Altium Designer.

This agreement adds an extra dimension for electronics designers working with FPGAs and provides seamless integration to industry proven VHDL and Verilog simulation from Aldec, tightly integrated within Altium Designer. Electronics designers can now access Aldec's proven simulation technology as part of Altium's unified electronics design architecture.

Nick Martin, CEO of Altium, said, "Altium Designer has included simple VHDL simulation for FPGA-based designs for a number of years, but this has not been a core focus area for us. Partnering with Aldec will allow us to provide a high-quality RTL simulation capability for Altium Designer users, both VHDL and Verilog, from a vendor with great technology and credentials in this area."

Dr. Stanley M. Hyduke, CEO of Aldec, said, "Becoming such a central element in simulation for Altium's unified approach to electronics design is an exciting step for Aldec. This agreement will open new opportunities for Aldec with design teams who traditionally would not invest in high-performance VHDL and Verilog simulation. Altium Designer's very broad user base and its FPGA vendor independence allow more designers to leverage Aldec simulations technology as part of Altium Designer's unified architecture."

Users of Altium's NanoBoard FPGA-based development boards will also now be able to create their FPGA-based design prototypes to prove the design concept, and then use the Aldec VHDL or Verilog simulation technology within Altium Designer to test their designs. (Both the NanoBoard NB2 and the NanoBoard 3000 are delivered with a 12-month Altium Designer Soft Design License, which will also include the Aldec OEM simulator from the next release of Altium Designer.)

Kylee Sealy, CEO of US-based design consultancy Enginuitive Technologies, said, "This news is very exciting for electronics designers at Enginuitive Technologies. We enjoy the vendor-neutral platform that Altium has provided, but we've had to use third-party tools to do all of our Verilog simulation. Now we can save time by doing all of our validation using Altium Designer and Aldec's integrated RTL solution. Our customers will be pleased to know that those savings will be passed directly to them. This is one more exciting way in which Altium has helped us to simplify the electronic design process!"

Aldec's OEM simulator will be the default VHDL or Verilog FPGA simulation technology in the next release of Altium Designer, currently scheduled for release in the next few weeks.

Information on [Altium Designer](#), the latest [new features](#), and those added over the past [12 months](#), is on Altium's [web site](#). More detailed information on Altium Designer is at the [Altium Wiki](#).

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About Altium

Altium Limited (ASX:ALU) provides next generation electronics design software that breaks down the barriers to innovation. Altium's unified electronics design environment links all aspects of electronics product design into one process, in a single application. This helps electronics designers harness the latest devices and technologies, manage their projects across broad design 'ecosystems', and create connected, intelligent designs.

Founded in 1985, Altium has headquarters in Sydney, sales offices in the United States, Europe, China, and resellers in all other major markets. For more information, visit www.altium.com.

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About Aldec

Aldec Incorporated is an industry-leader in Electronic Design Verification and offers a patented technology suite including: RTL Design, RTL Simulators, Hardware-Assisted Verification, Design Rule Checking, IP Cores, DO-254 Functional Verification and Military/Aerospace solutions. www.aldec.com