



FPGA Multiplexer Resource Usage

Summary

This quick reference provides detailed information about resource usage of all pre-synthesized Multiplexer cores.

Core Reference
CR0137 (v1.03) June 6, 2005

Multiplexer

The available Multiplexer cores are listed as follows:

- [1-Bit Multiplexers](#)
- [2-Bits Multiplexers](#)
- [3-Bits Multiplexers](#)
- [4-Bits Multiplexers](#)
- [5-Bits Multiplexers](#)
- [6-Bits Multiplexers](#)
- [7-Bits Multiplexers](#)
- [8-Bits Multiplexers](#)
- [9-Bits Multiplexers](#)
- [10-Bits Multiplexers](#)
- [12-Bits Multiplexers](#)
- [16-Bits Multiplexers](#)
- [32-Bits Multiplexers](#)

1-Bit Multiplexers

The available 1-Bit Multiplexers cores are listed as follows:

[M1_B2S1](#)

[M1_B2S1E](#)

[M1_B4S1](#)

[M1_B4S1_SB](#)

[M1_B4S1E](#)

[M1_B4S1E_SB](#)

[M1_B8S1](#)

[M1_B8S1_SB](#)

FPGA Multiplexer Resource Usage

M1_B8S1E	M1_B8S1E_SB	M1_B16S1	M1_B16S1_SB
M1_B16S1E	M1_B16S1E_SB	M1_S1B2	M1_S1B2E
M1_S1B4	M1_S1B4_SB	M1_S1B4E	M1_S1B4E_SB
M1_S1B8	M1_S1B8_SB	M1_S1B8E	M1_S1B8E_SB
M1_S1B16	M1_S1B16_SB	M1_S1B16E	M1_S1B16E_SB
M1_S1S2	M1_S1S2E	M1_S1S4	M1_S1S4_SB
M1_S1S4E	M1_S1S4E_SB	M1_S1S8	M1_S1S8_SB
M1_S1S8E	M1_S1S8E_SB	M1_S1S16	M1_S1S16_SB
M1_S1S16E	M1_S1S16E_SB	M1_S2S1	M1_S2S1E
M1_S4S1	M1_S4S1_SB	M1_S4S1E	M1_S4S1E_SB
M1_S8S1	M1_S8S1_SB	M1_S8S1E	M1_S8S1E_SB
M1_S16S1	M1_S16S1_SB	M1_S16S1E	M1_S16S1E_SB

2-Bits Multiplexers

The available 2-Bits Multiplexers cores are listed as follows:

M2_B1B2	M2_B1B2E	M2_B1B4	M2_B1B4_SB
M2_B1B4E	M2_B1B4E_SB	M2_B1B8	M2_B1B8_SB
M2_B1B8E	M2_B1B8E_SB	M2_B1B16	M2_B1B16_SB
M2_B1B16E	M2_B1B16E_SB	M2_B2B1	M2_B2B1E
M2_B4B1	M2_B4B1_SB	M2_B4B1E	M2_B4B1E_SB
M2_B8B1	M2_B8B1_SB	M2_B8B1E	M2_B8B1E_SB
M2_B16B1	M2_B16B1_SB	M2_B16B1E	M2_B16B1E_SB
M2_S1S2	M2_S1S2E	M2_S1S4	M2_S1S4_SB
M2_S1S4E	M2_S1S4E_SB	M2_S1S8	M2_S1S8_SB
M2_S1S8E	M2_S1S8E_SB	M2_S2S1	M2_S2S1E
M2_S4S1	M2_S4S1_SB	M2_S4S1E	M2_S4S1E_SB
M2_S8S1	M2_S8S1_SB	M2_S8S1E	M2_S8S1E_SB

3-Bits Multiplexers

The available 3-Bits Multiplexers cores are listed as follows:

M3_B1B2	M3_B1B2E	M3_B1B4	M3_B1B4_SB
M3_B1B4E	M3_B1B4E_SB	M3_B1B8	M3_B1B8_SB
M3_B1B8E	M3_B1B8E_SB	M3_B1B16	M3_B1B16_SB
M3_B1B16E	M3_B1B16E_SB	M3_B2B1	M3_B2B1E
M3_B4B1	M3_B4B1_SB	M3_B4B1E	M3_B4B1E_SB
M3_B8B1	M3_B8B1_SB	M3_B8B1E	M3_B8B1E_SB
M3_B16B1	M3_B16B1_SB	M3_B16B1E	M3_B16B1E_SB
M3_S1S2	M3_S1S2E	M3_S1S4	M3_S1S4_SB
M3_S1S4E	M3_S1S4E_SB	M3_S2S1	M3_S2S1E
M3_S4S1	M3_S4S1_SB	M3_S4S1E	M3_S4S1E_SB

4-Bits Multiplexers

The available 4-Bits Multiplexers cores are listed as follows:

M4_B1B2	M4_B1B2E	M4_B1B4	M4_B1B4_SB
M4_B1B4E	M4_B1B4E_SB	M4_B1B8	M4_B1B8_SB
M4_B1B8E	M4_B1B8E_SB	M4_B1B16	M4_B1B16_SB
M4_B1B16E	M4_B1B16E_SB	M4_B2B1	M4_B2B1E
M4_B4B1	M4_B4B1_SB	M4_B4B1E	M4_B4B1E_SB
M4_B8B1	M4_B8B1_SB	M4_B8B1E	M4_B8B1E_SB
M4_B16B1	M4_B16B1_SB	M4_B16B1E	M4_B16B1E_SB
M4_S1S2	M4_S1S2E	M4_S1S4	M4_S1S4_SB
M4_S1S4E	M4_S1S4E_SB	M4_S2S1	M4_S2S1E
M4_S4S1	M4_S4S1_SB	M4_S4S1E	M4_S4S1E_SB

FPGA Multiplexer Resource Usage

5-Bits Multiplexers

The available 5-Bits Multiplexers cores are listed as follows:

M5_B1B2	M5_B1B2E	M5_B1B4	M5_B1B4_SB
M5_B1B4E	M5_B1B4E_SB	M5_B1B8	M5_B1B8_SB
M5_B1B8E	M5_B1B8E_SB	M5_B1B16	M5_B1B16_SB
M5_B1B16E	M5_B1B16E_SB	M5_B2B1	M5_B2B1E
M5_B4B1	M5_B4B1_SB	M5_B4B1E	M5_B4B1E_SB
M5_B8B1	M5_B8B1_SB	M5_B8B1E	M5_B8B1E_SB
M5_B16B1	M5_B16B1_SB	M5_B16B1E	M5_B16B1E_SB
M5_S1S2	M5_S1S2E	M5_S2S1	M5_S2S1E

6-Bits Multiplexers

The available 6-Bits Multiplexers cores are listed as follows:

M6_B1B2	M6_B1B2E	M6_B1B4	M6_B1B4_SB
M6_B1B4E	M6_B1B4E_SB	M6_B1B8	M6_B1B8_SB
M6_B1B8E	M6_B1B8E_SB	M6_B1B16	M6_B1B16_SB
M6_B1B16E	M6_B1B16E_SB	M6_B2B1	M6_B2B1E
M6_B4B1	M6_B4B1_SB	M6_B4B1E	M6_B4B1E_SB
M6_B8B1	M6_B8B1_SB	M6_B8B1E	M6_B8B1E_SB
M6_B16B1	M6_B16B1_SB	M6_B16B1E	M6_B16B1E_SB
M6_S1S2	M6_S1S2E	M6_S2S1	M6_S2S1E

7-Bits Multiplexers

The available 7-Bits Multiplexers cores are listed as follows:

M7_B1B2	M7_B1B2E	M7_B1B4	M7_B1B4_SB
M7_B1B4E	M7_B1B4E_SB	M7_B1B8	M7_B1B8_SB

<u>M7_B1B8E</u>	<u>M7_B1B8E_SB</u>	<u>M7_B1B16</u>	<u>M7_B1B16_SB</u>
<u>M7_B1B16E</u>	<u>M7_B1B16E_SB</u>	<u>M7_B2B1</u>	<u>M7_B2B1E</u>
<u>M7_B4B1</u>	<u>M7_B4B1_SB</u>	<u>M7_B4B1E</u>	<u>M7_B4B1E_SB</u>
<u>M7_B8B1</u>	<u>M7_B8B1_SB</u>	<u>M7_B8B1E</u>	<u>M7_B8B1E_SB</u>
<u>M7_B16B1</u>	<u>M7_B16B1_SB</u>	<u>M7_B16B1E</u>	<u>M7_B16B1E_SB</u>
<u>M7_S1S2</u>	<u>M7_S1S2E</u>	<u>M7_S2S1</u>	<u>M7_S2S1E</u>

8-Bits Multiplexers

The available 8-Bits Multiplexers cores are listed as follows:

<u>M8_B1B2</u>	<u>M8_B1B2E</u>	<u>M8_B1B4</u>	<u>M8_B1B4_SB</u>
<u>M8_B1B4E</u>	<u>M8_B1B4E_SB</u>	<u>M8_B1B8</u>	<u>M8_B1B8_SB</u>
<u>M8_B1B8E</u>	<u>M8_B1B8E_SB</u>	<u>M8_B1B16</u>	<u>M8_B1B16_SB</u>
<u>M8_B1B16E</u>	<u>M8_B1B16E_SB</u>	<u>M8_B2B1</u>	<u>M8_B2B1E</u>
<u>M8_B4B1</u>	<u>M8_B4B1_SB</u>	<u>M8_B4B1E</u>	<u>M8_B4B1E_SB</u>
<u>M8_B8B1</u>	<u>M8_B8B1_SB</u>	<u>M8_B8B1E</u>	<u>M8_B8B1E_SB</u>
<u>M8_B16B1</u>	<u>M8_B16B1_SB</u>	<u>M8_B16B1E</u>	<u>M8_B16B1E_SB</u>
<u>M8_S1S2</u>	<u>M8_S1S2E</u>	<u>M8_S2S1</u>	<u>M8_S2S1E</u>

9-Bits Multiplexers

The available 9-Bits Multiplexers cores are listed as follows:

<u>M9_B1B2</u>	<u>M9_B1B2E</u>	<u>M9_B1B4</u>	<u>M9_B1B4_SB</u>
<u>M9_B1B4E</u>	<u>M9_B1B4E_SB</u>	<u>M9_B1B8</u>	<u>M9_B1B8_SB</u>
<u>M9_B1B8E</u>	<u>M9_B1B8E_SB</u>	<u>M9_B1B16</u>	<u>M9_B1B16_SB</u>
<u>M9_B1B16E</u>	<u>M9_B1B16E_SB</u>	<u>M9_B2B1</u>	<u>M9_B2B1E</u>
<u>M9_B4B1</u>	<u>M9_B4B1_SB</u>	<u>M9_B4B1E</u>	<u>M9_B4B1E_SB</u>
<u>M9_B8B1</u>	<u>M9_B8B1_SB</u>	<u>M9_B8B1E</u>	<u>M9_B8B1E_SB</u>
<u>M9_B16B1</u>	<u>M9_B16B1_SB</u>	<u>M9_B16B1E</u>	<u>M9_B16B1E_SB</u>

10-Bits Multiplexers

The available 10-Bits Multiplexers cores are listed as follows:

M10_B1B2	M10_B1B2E	M10_B1B4	M10_B1B4_SB
M10_B1B4E	M10_B1B4E_SB	M10_B1B8	M10_B1B8_SB
M10_B1B8E	M10_B1B8E_SB	M10_B1B16	M10_B1B16_SB
M10_B1B16E	M10_B1B16E_SB	M10_B2B1	M10_B2B1E
M10_B4B1	M10_B4B1_SB	M10_B4B1E	M10_B4B1E_SB
M10_B8B1	M10_B8B1_SB	M10_B8B1E	M10_B8B1E_SB
M10_B16B1	M10_B16B1_SB	M10_B16B1E	M10_B16B1E_SB

12-Bits Multiplexers

The available 12-Bits Multiplexers cores are listed as follows:

M12_B1B2	M12_B1B2E	M12_B1B4	M12_B1B4_SB
M12_B1B4E	M12_B1B4E_SB	M12_B1B8	M12_B1B8_SB
M12_B1B8E	M12_B1B8E_SB	M12_B1B16	M12_B1B16_SB
M12_B1B16E	M12_B1B16E_SB	M12_B2B1	M12_B2B1E
M12_B4B1	M12_B4B1_SB	M12_B4B1E	M12_B4B1E_SB
M12_B8B1	M12_B8B1_SB	M12_B8B1E	M12_B8B1E_SB
M12_B16B1	M12_B16B1_SB	M12_B16B1E	M12_B16B1E_SB

16-Bits Multiplexers

The available 16-Bits Multiplexers cores are listed as follows:

M16_B1B2	M16_B1B2E	M16_B1B4	M16_B1B4_SB
M16_B1B4E	M16_B1B4E_SB	M16_B1B8	M16_B1B8_SB

M16_B1B8E	M16_B1B8E_SB	M16_B1B16	M16_B1B16_SB
M16_B1B16E	M16_B1B16E_SB	M16_B2B1	M16_B2B1E
M16_B4B1	M16_B4B1_SB	M16_B4B1E	M16_B4B1E_SB
M16_B8B1	M16_B8B1_SB	M16_B8B1E	M16_B8B1E_SB
M16_B16B1	M16_B16B1_SB	M16_B16B1E	M16_B16B1E_SB

32-Bits Multiplexers

The available 32-Bits Multiplexers cores are listed as follows:

M32_B1B2	M32_B1B2E	M32_B1B4	M32_B1B4_SB
M32_B1B4E	M32_B1B4E_SB	M32_B1B8	M32_B1B8_SB
M32_B1B8E	M32_B1B8E_SB	M32_B1B16	M32_B1B16_SB
M32_B1B16E	M32_B1B16E_SB	M32_B2B1	M32_B2B1E
M32_B4B1	M32_B4B1_SB	M32_B4B1E	M32_B4B1E_SB
M32_B8B1	M32_B8B1_SB	M32_B8B1E	M32_B8B1E_SB
M32_B16B1	M32_B16B1_SB	M32_B16B1E	M32_B16B1E_SB

M1_B2S1

1x2-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		1	1									
Stratix		1	1									
Stratix2		1	1									
Max2		1	1									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	1									
Spartan2E		2	1									
Spartan3		2	1									
Virtex		2	1									
Virtex2		2	1									
Virtex2p		2	1									
VirtexE		2	1									
Virtex4		2	1									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		1										

M1_B2S1E**1x2-Bit Bus to 1x1-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		1	1									
Stratix		1	1									
Stratix2		1	1									
Max2		1	1									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	1									
Spartan2E		2	1									
Spartan3		2	1									
Virtex		2	1									
Virtex2		2	1									
Virtex2p		2	1									
VirtexE		2	1									
Virtex4		2	1									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		2										

M1_B4S1

1x4-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		1	1									
Max2		2	2									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		3										

M1_B4S1_SB**1x4-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		1	1									
Max2		2	2									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		3										

M1_B4S1E

1x4-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		1	1									
Max2		3	3									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		4										

M1_B4S1E_SB**1x4-Bit Bus to 1x1-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		1	1									
Max2		3	3									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		4										

M1_B8S1

1x8-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		3	3									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_B8S1_SB**1x8-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		3	3									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_B8S1E

1x8-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		4	4									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_B8S1E_SB**1x8-Bit Bus to 1x1-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		4	4									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_B16S1

1x16-Bit Bus to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		8	8									
Virtex		10	9									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		10	9									
Virtex4		8	8									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_B16S1_SB**1x16-Bit Bus to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		8	8									
Virtex		10	9									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		10	9									
Virtex4		8	8									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_B16S1E

1x16-Bit Bus to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		11	11									
Stratix		11	11									
Stratix2		5	5									
Max2		11	11									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		14	13									
Spartan2E		14	13									
Spartan3		14	13									
Virtex		14	13									
Virtex2		14	13									
Virtex2p		14	13									
VirtexE		14	13									
Virtex4		14	13									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_B16S1E_SB**1x16-Bit Bus to 1x1-Single Wire Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		11	11									
Stratix		11	11									
Stratix2		5	5									
Max2		11	11									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		14	13									
Spartan2E		14	13									
Spartan3		14	13									
Virtex		14	13									
Virtex2		14	13									
Virtex2p		14	13									
VirtexE		14	13									
Virtex4		14	13									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_S1B2

1x1-Single Wire to 1x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		2										

M1_S1B2E**1x1-Single Wire to 1x2-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		3										

M1_S1B4

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M1_S1B4_SB**1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M1_S1B4E

1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		7										

M1_S1B4E_SB**1x1-Single Wire to 1x4-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		7										

M1_S1B8

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M1_S1B8_SB**1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M1_S1B8E

1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		8	8									
Max2		9	9									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		15										

M1_S1B8E_SB**1x1-Single Wire to 1x8-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		8	8									
Max2		9	9									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		15										

M1_S1B16

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M1_S1B16_SB**1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M1_S1B16E

1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		17	17									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		27										

M1_S1B16E_SB**1x1-Single Wire to 1x16-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		17	17									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		27										

M1_S1S2

1x1-Single Wire to 2x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		2										

M1_S1S2E**1x1-Single Wire to 2x1-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		3										

M1_S1S4

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M1_S1S4_SB**1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M1_S1S4E

1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		7										

M1_S1S4E_SB**1x1-Single Wire to 4x1-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		7										

M1_S1S8

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M1_S1S8_SB**1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M1_S1S8E

1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		8	8									
Max2		9	9									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		15										

M1_S1S8E_SB**1x1-Single Wire to 8x1-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		8	8									
Max2		9	9									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		15										

M1_S1S16

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M1_S1S16_SB**1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex)
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M1_S1S16E

1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		17	17									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		27										

M1_S1S16E_SB**1x1-Single Wire to 16x1-Single Wire Multiplexer (Demultiplex)
With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		17	17									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		27										

M1_S2S1

2x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		1	1									
Stratix		1	1									
Stratix2		1	1									
Max2		1	1									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	1									
Spartan2E		2	1									
Spartan3		2	1									
Virtex		2	1									
Virtex2		2	1									
Virtex2p		2	1									
VirtexE		2	1									
Virtex4		2	1									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		1										

M1_S2S1E**2x1-Single Wire to 1x1-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		1	1									
Stratix		1	1									
Stratix2		1	1									
Max2		1	1									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	1									
Spartan2E		2	1									
Spartan3		2	1									
Virtex		2	1									
Virtex2		2	1									
Virtex2p		2	1									
VirtexE		2	1									
Virtex4		2	1									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		2										

M1_S4S1

4x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		1	1									
Max2		2	2									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		3										

M1_S4S1_SB**4x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		1	1									
Max2		2	2									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		3										

M1_S4S1E

4x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		1	1									
Max2		3	3									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		4										

M1_S4S1E_SB**4x1-Single Wire to 1x1-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		1	1									
Max2		3	3									
Max3000a	1	1										
Max7000b	1	1										
Max7000ae	1	1										
Max7000s	1	1										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		4										

M1_S8S1

8x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		3	3									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_S8S1_SB**8x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		3	3									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_S8S1E

8x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		4	4									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_S8S1E_SB**8x1-Single Wire to 1x1-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		4	4									
Max2		5	5									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		11										

M1_S16S1

16x1-Single Wire to 1x1-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		8	8									
Virtex		10	9									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		10	9									
Virtex4		8	8									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_S16S1_SB**16x1-Single Wire to 1x1-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		8	8									
Virtex		10	9									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		10	9									
Virtex4		8	8									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_S16S1E

16x1-Single Wire to 1x1-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		11	11									
Stratix		11	11									
Stratix2		5	5									
Max2		11	11									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		14	13									
Spartan2E		14	13									
Spartan3		14	13									
Virtex		14	13									
Virtex2		14	13									
Virtex2p		14	13									
VirtexE		14	13									
Virtex4		14	13									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M1_S16S1E_SB**16x1-Single Wire to 1x1-Single Wire Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		11	11									
Stratix		11	11									
Stratix2		5	5									
Max2		11	11									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		14	13									
Spartan2E		14	13									
Spartan3		14	13									
Virtex		14	13									
Virtex2		14	13									
Virtex2p		14	13									
VirtexE		14	13									
Virtex4		14	13									
CoolRunner2	1											
CoolRunnerXpla3	1											
Xc9500	1											
Xc9500XL	1											
Xc9500XV	1											
ProAsicPlus		19										

M2_B1B2

1x2-Bit Bus to 2x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		4										

M2_B1B2E**1x2-Bit Bus to 2x2-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M2_B1B4

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		12										

M2_B1B4_SB**1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		12										

M2_B1B4E

1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M2_B1B4E_SB**1x2-Bit Bus to 4x2-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M2_B1B8

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		24										

M2_B1B8_SB**1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		24										

M2_B1B8E

1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M2_B1B8E_SB**1x2-Bit Bus to 8x2-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M2_B1B16

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		32	32									
Max2		36	36									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		48										

M2_B1B16_SB**1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		32	32									
Max2		36	36									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		48										

M2_B1B16E

1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		38	38									
Stratix		38	38									
Stratix2		34	34									
Max2		38	38									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		38	38									
Spartan2E		38	38									
Spartan3		38	38									
Virtex		38	38									
Virtex2		38	38									
Virtex2p		38	38									
VirtexE		38	38									
Virtex4		38	38									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		50										

M2_B1B16E_SB**1x2-Bit Bus to 16x2-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		38	38									
Stratix		38	38									
Stratix2		34	34									
Max2		38	38									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		38	38									
Spartan2E		38	38									
Spartan3		38	38									
Virtex		38	38									
Virtex2		38	38									
Virtex2p		38	38									
VirtexE		38	38									
Virtex4		38	38									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		50										

M2_B2B1

2x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		2										

M2_B2B1E**2x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		4										

M2_B4B1

4x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		2	2									
Max2		4	4									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		6										

M2_B4B1_SB

4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		2	2									
Max2		4	4									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		6										

M2_B4B1E

4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		2	2									
Max2		6	6									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		8										

M2_B4B1E_SB**4x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		2	2									
Max2		6	6									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		8										

M2_B8B1

8x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		28										

M2_B8B1_SB

8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		28										

M2_B8B1E

8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		30										

M2_B8B1E_SB**8x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		30										

M2_B16B1

16x2-Bit Bus to 1x2-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		10	10									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		16	16									
Virtex		18	18									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		18	18									
Virtex4		16	16									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		56										

M2_B16B1_SB

16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		10	10									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		16	16									
Virtex		18	18									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		18	18									
Virtex4		16	16									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		56										

M2_B16B1E

16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		22	22									
Stratix		22	22									
Stratix2		10	10									
Max2		22	22									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		24	23									
Spartan2E		24	23									
Spartan3		24	23									
Virtex		24	23									
Virtex2		24	23									
Virtex2p		24	23									
VirtexE		24	23									
Virtex4		24	23									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		58										

M2_B16B1E_SB**16x2-Bit Bus to 1x2-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		22	22									
Stratix		22	22									
Stratix2		10	10									
Max2		22	22									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		24	23									
Spartan2E		24	23									
Spartan3		24	23									
Virtex		24	23									
Virtex2		24	23									
Virtex2p		24	23									
VirtexE		24	23									
Virtex4		24	23									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		58										

M2_S1S2

1x2-Single Wire to 2x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		4										

M2_S1S2E**1x2-Single Wire to 2x2-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		6										

M2_S1S4

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		12										

M2_S1S4_SB**1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		12										

M2_S1S4E

1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M2_S1S4E_SB**1x2-Single Wire to 4x2-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		14										

M2_S1S8

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		24										

M2_S1S8_SB**1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		24										

M2_S1S8E

1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M2_S1S8E_SB**1x2-Single Wire to 8x2-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		16	16									
Max2		18	18									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		26										

M2_S2S1

2x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		2										

M2_S2S1E**2x2-Single Wire to 1x2-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		2	2									
Stratix		2	2									
Stratix2		2	2									
Max2		2	2									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		2	2									
Spartan2E		2	2									
Spartan3		2	2									
Virtex		2	2									
Virtex2		2	2									
Virtex2p		2	2									
VirtexE		2	2									
Virtex4		2	2									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		4										

M2_S4S1

4x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		2	2									
Max2		4	4									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		6										

M2_S4S1_SB**4x2-Single Wire to 1x2-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		2	2									
Max2		4	4									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		6										

M2_S4S1E

4x2-Single Wire to 1x2-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		2	2									
Max2		6	6									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		8										

M2_S4S1E_SB**4x2-Single Wire to 1x2-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		2	2									
Max2		6	6									
Max3000a	2	2										
Max7000b	2	2										
Max7000ae	2	2										
Max7000s	2	2										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		8										

M2_S8S1

8x2-Single Wire to 1x2-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		28										

M2_S8S1_SB**8x2-Single Wire to 1x2-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		28										

M2_S8S1E

8x2-Single Wire to 1x2-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		30										

M2_S8S1E_SB**8x2-Single Wire to 1x2-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		6	6									
Max2		10	10									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	2											
CoolRunnerXpla3	2											
Xc9500	2											
Xc9500XL	2											
Xc9500XV	2											
ProAsicPlus		30										

M3_B1B2

1x3-Bit Bus to 2x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		6										

M3_B1B2E

1x3-Bit Bus to 2x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		8										

M3_B1B4

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		16										

M3_B1B4_SB

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		16										

M3_B1B4E

1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		18										

M3_B1B4E_SB**1x3-Bit Bus to 4x3-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		18										

M3_B1B8

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		34										

M3_B1B8_SB**1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		34										

M3_B1B8E

1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		26	26									
Stratix		26	26									
Stratix2		24	24									
Max2		26	26									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		26	26									
Spartan2E		26	26									
Spartan3		26	26									
Virtex		26	26									
Virtex2		26	26									
Virtex2p		26	26									
VirtexE		26	26									
Virtex4		26	26									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		36										

M3_B1B8E_SB**1x3-Bit Bus to 8x3-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		26	26									
Stratix		26	26									
Stratix2		24	24									
Max2		26	26									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		26	26									
Spartan2E		26	26									
Spartan3		26	26									
Virtex		26	26									
Virtex2		26	26									
Virtex2p		26	26									
VirtexE		26	26									
Virtex4		26	26									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		36										

M3_B1B16

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		52	52									
Stratix		52	52									
Stratix2		48	48									
Max2		52	52									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		52	52									
Spartan2E		52	52									
Spartan3		52	52									
Virtex		52	52									
Virtex2		52	52									
Virtex2p		52	52									
VirtexE		52	52									
Virtex4		52	52									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		68										

M3_B1B16_SB**1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		52	52									
Stratix		52	52									
Stratix2		48	48									
Max2		52	52									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		52	52									
Spartan2E		52	52									
Spartan3		52	52									
Virtex		52	52									
Virtex2		52	52									
Virtex2p		52	52									
VirtexE		52	52									
Virtex4		52	52									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		68										

M3_B1B16E

1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		52	52									
Stratix		52	52									
Stratix2		50	50									
Max2		52	52									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		52	52									
Spartan2E		52	52									
Spartan3		52	52									
Virtex		52	52									
Virtex2		52	52									
Virtex2p		52	52									
VirtexE		52	52									
Virtex4		52	52									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		70										

M3_B1B16E_SB**1x3-Bit Bus to 16x3-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		52	52									
Stratix		52	52									
Stratix2		50	50									
Max2		52	52									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		52	52									
Spartan2E		52	52									
Spartan3		52	52									
Virtex		52	52									
Virtex2		52	52									
Virtex2p		52	52									
VirtexE		52	52									
Virtex4		52	52									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		70										

M3_B2B1

2x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		3	3									
Max2		3	3									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		3										

M3_B2B1E**2x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		3	3									
Max2		3	3									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		6										

M3_B4B1

4x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		3	3									
Max2		6	6									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		9										

M3_B4B1_SB

4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		3	3									
Max2		6	6									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		9										

M3_B4B1E

4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		3	3									
Max2		9	9									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		12										

M3_B4B1E_SB**4x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		3	3									
Max2		9	9									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		12										

M3_B8B1

8x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		9	9									
Max2		15	15									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		42										

M3_B8B1_SB

8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		9	9									
Max2		15	15									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		42										

M3_B8B1E

8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		9	9									
Max2		15	15									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		16	15									
Spartan2E		16	15									
Spartan3		16	15									
Virtex		16	15									
Virtex2		16	15									
Virtex2p		16	15									
VirtexE		16	15									
Virtex4		16	15									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		45										

M3_B8B1E_SB**8x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		9	9									
Max2		15	15									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		16	15									
Spartan2E		16	15									
Spartan3		16	15									
Virtex		16	15									
Virtex2		16	15									
Virtex2p		16	15									
VirtexE		16	15									
Virtex4		16	15									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		45										

M3_B16B1

16x3-Bit Bus to 1x3-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		15	15									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		28	27									
Spartan2E		28	27									
Spartan3		24	24									
Virtex		28	27									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		28	27									
Virtex4		24	24									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		87										

M3_B16B1_SB

16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		15	15									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		28	27									
Spartan2E		28	27									
Spartan3		24	24									
Virtex		28	27									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		28	27									
Virtex4		24	24									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		87										

M3_B16B1E

16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		33	33									
Stratix		33	33									
Stratix2		15	15									
Max2		33	33									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		34	33									
Spartan2E		34	33									
Spartan3		34	33									
Virtex		34	33									
Virtex2		34	33									
Virtex2p		34	33									
VirtexE		34	33									
Virtex4		34	33									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		87										

M3_B16B1E_SB**16x3-Bit Bus to 1x3-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		33	33									
Stratix		33	33									
Stratix2		15	15									
Max2		33	33									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		34	33									
Spartan2E		34	33									
Spartan3		34	33									
Virtex		34	33									
Virtex2		34	33									
Virtex2p		34	33									
VirtexE		34	33									
Virtex4		34	33									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		87										

M3_S1S2

1x3-Single Wire to 2x3-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		6										

M3_S1S2E**1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		8										

M3_S1S4

1x3-Single Wire to 4x3-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		16										

M3_S1S4_SB**1x3-Single Wire to 4x3-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		16										

M3_S1S4E

1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		18										

M3_S1S4E_SB**1x3-Single Wire to 1x3-Single Wire Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		18										

M3_S2S1

2x3-Single Wire to 1x3-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		3	3									
Max2		3	3									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		3										

M3_S2S1E**2x3-Single Wire to 2x3-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		3	3									
Stratix		3	3									
Stratix2		3	3									
Max2		3	3									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		4	3									
Spartan2E		4	3									
Spartan3		4	3									
Virtex		4	3									
Virtex2		4	3									
Virtex2p		4	3									
VirtexE		4	3									
Virtex4		4	3									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		6										

M3_S4S1

4x3-Single Wire to 1x3-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		3	3									
Max2		6	6									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		9										

M3_S4S1_SB**4x3-Single Wire to 1x3-Single Wire Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		3	3									
Max2		6	6									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		9										

M3_S4S1E

4x3-Single Wire to 4x3-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		3	3									
Max2		9	9									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		12										

M3_S4S1E_SB

4x3-Single Wire to 4x3-Single Wire Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		3	3									
Max2		9	9									
Max3000a	3	3										
Max7000b	3	3										
Max7000ae	3	3										
Max7000s	3	3										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	3											
CoolRunnerXpla3	3											
Xc9500	3											
Xc9500XL	3											
Xc9500XV	3											
ProAsicPlus		12										

M4_B1B2

1x4-Bit Bus to 2x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		8										

M4_B1B2E

1x4-Bit Bus to 2x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		10										

M4_B1B4

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		20										

M4_B1B4_SB

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		20										

M4_B1B4E

1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										

M4_B1B4E_SB**1x4-Bit Bus to 4x4-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										

M4_B1B8

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		44										

M4_B1B8_SB**1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		44										

M4_B1B8E

1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		34	34									
Stratix		34	34									
Stratix2		32	32									
Max2		34	34									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		34	34									
Spartan2E		34	34									
Spartan3		34	34									
Virtex		34	34									
Virtex2		34	34									
Virtex2p		34	34									
VirtexE		34	34									
Virtex4		34	34									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		46										

M4_B1B8E_SB**1x4-Bit Bus to 8x4-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		34	34									
Stratix		34	34									
Stratix2		32	32									
Max2		34	34									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		34	34									
Spartan2E		34	34									
Spartan3		34	34									
Virtex		34	34									
Virtex2		34	34									
Virtex2p		34	34									
VirtexE		34	34									
Virtex4		34	34									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		46										

M4_B1B16

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		68	68									
Stratix		68	68									
Stratix2		64	64									
Max2		68	68									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		68	68									
Spartan2E		68	68									
Spartan3		68	68									
Virtex		68	68									
Virtex2		68	68									
Virtex2p		68	68									
VirtexE		68	68									
Virtex4		68	68									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		88										

M4_B1B16_SB**1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		68	68									
Stratix		68	68									
Stratix2		64	64									
Max2		68	68									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		68	68									
Spartan2E		68	68									
Spartan3		68	68									
Virtex		68	68									
Virtex2		68	68									
Virtex2p		68	68									
VirtexE		68	68									
Virtex4		68	68									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		88										

M4_B1B16E

1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		68	68									
Stratix		68	68									
Stratix2		66	66									
Max2		68	68									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		68	68									
Spartan2E		68	68									
Spartan3		68	68									
Virtex		68	68									
Virtex2		68	68									
Virtex2p		68	68									
VirtexE		68	68									
Virtex4		68	68									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		90										

M4_B1B16E_SB**1x4-Bit Bus to 16x4-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		68	68									
Stratix		68	68									
Stratix2		66	66									
Max2		68	68									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		68	68									
Spartan2E		68	68									
Spartan3		68	68									
Virtex		68	68									
Virtex2		68	68									
Virtex2p		68	68									
VirtexE		68	68									
Virtex4		68	68									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		90										

M4_B2B1

2x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		4										

M4_B2B1E

2x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		8										

M4_B4B1

4x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		4	4									
Max2		8	8									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		20										

M4_B4B1_SB

4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		4	4									
Max2		8	8									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		20										

M4_B4B1E

4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		4	4									
Max2		12	12									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		24										

M4_B4B1E_SB**4x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		4	4									
Max2		12	12									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		24										

M4_B8B1

8x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		12	12									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		52										

M4_B8B1_SB

8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		12	12									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		52										

M4_B8B1E

8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		12	12									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		56										

M4_B8B1E_SB**8x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		12	12									
Max2		20	20									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		56										

M4_B16B1

16x4-Bit Bus to 1x4-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		20	20									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		32	32									
Virtex		36	36									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		36	36									
Virtex4		32	32									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		108										

M4_B16B1_SB**16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		20	20									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		32	32									
Virtex		36	36									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		36	36									
Virtex4		32	32									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		108										

M4_B16B1E

16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		44	44									
Stratix		44	44									
Stratix2		20	20									
Max2		44	44									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		44	43									
Spartan2E		44	43									
Spartan3		44	43									
Virtex		44	43									
Virtex2		44	43									
Virtex2p		44	43									
VirtexE		44	43									
Virtex4		44	43									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		108										

M4_B16B1E_SB**16x4-Bit Bus to 1x4-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		44	44									
Stratix		44	44									
Stratix2		20	20									
Max2		44	44									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		44	43									
Spartan2E		44	43									
Spartan3		44	43									
Virtex		44	43									
Virtex2		44	43									
Virtex2p		44	43									
VirtexE		44	43									
Virtex4		44	43									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		108										

M4_S1S2

1x4-Single Wire to 2x4-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		8										

M4_S1S2E**1x4-Single Wire to 2x4-Single Wire Multiplexer (Demultiplex)
With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		10										

M4_S1S4

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		20										

M4_S1S4_SB**1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		20										

M4_S1S4E

1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										

M4_S1S4E_SB**1x4-Single Wire to 4x4-Single Wire Multiplexer (Demultiplex)
With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		22										

M4_S2S1

2x4-Single Wire to 1x4-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		4										

M4_S2S1E**2x4-Single Wire to 1x4-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		4	4									
Stratix		4	4									
Stratix2		4	4									
Max2		4	4									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		4	4									
Spartan2E		4	4									
Spartan3		4	4									
Virtex		4	4									
Virtex2		4	4									
Virtex2p		4	4									
VirtexE		4	4									
Virtex4		4	4									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		8										

M4_S4S1

4x4-Single Wire to 1x4-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		4	4									
Max2		8	8									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		12										

M4_S4S1_SB

4x4-Single Wire to 1x4-Single Wire Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		4	4									
Max2		8	8									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		12										

M4_S4S1E

4x4-Single Wire to 1x4-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		4	4									
Max2		12	12									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		16										

M4_S4S1E_SB**4x4-Single Wire to 1x4-Single Wire Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		4	4									
Max2		12	12									
Max3000a	4	4										
Max7000b	4	4										
Max7000ae	4	4										
Max7000s	4	4										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	4											
CoolRunnerXpla3	4											
Xc9500	4											
Xc9500XL	4											
Xc9500XV	4											
ProAsicPlus		16										

M5_B1B2

1x5-Bit Bus to 2x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		10										

M5_B1B2E**1x5-Bit Bus to 2x5-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		12										

M5_B1B4

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		20	20									
Max2		20	20									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	20											
CoolRunnerXpla3	20											
Xc9500	20											
Xc9500XL	20											
Xc9500XV	20											
ProAsicPlus		24										

M5_B1B4_SB**1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements		4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20										
Stratix		20	20										
Stratix2		20	20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		24											

M5_B1B4E

1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		20	20									
Max2		20	20									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	20											
CoolRunnerXpla3	20											
Xc9500	20											
Xc9500XL	20											
Xc9500XV	20											
ProAsicPlus		26										

M5_B1B4E_SB**1x5-Bit Bus to 4x5-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		20	20									
Max2		20	20									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	20											
CoolRunnerXpla3	20											
Xc9500	20											
Xc9500XL	20											
Xc9500XV	20											
ProAsicPlus		26										

M5_B1B8

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		52										

M5_B1B8_SB**1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		52										

M5_B1B8E

1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		42	42									
Stratix		42	42									
Stratix2		40	40									
Max2		42	42									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		42	42									
Spartan2E		42	42									
Spartan3		42	42									
Virtex		42	42									
Virtex2		42	42									
Virtex2p		42	42									
VirtexE		42	42									
Virtex4		42	42									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		54										

M5_B1B8E_SB**1x5-Bit Bus to 8x5-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		42	42									
Stratix		42	42									
Stratix2		40	40									
Max2		42	42									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		42	42									
Spartan2E		42	42									
Spartan3		42	42									
Virtex		42	42									
Virtex2		42	42									
Virtex2p		42	42									
VirtexE		42	42									
Virtex4		42	42									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		54										

M5_B1B16

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		84	84									
Stratix		84	84									
Stratix2		80	80									
Max2		84	84									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		84	84									
Spartan2E		84	84									
Spartan3		84	84									
Virtex		84	84									
Virtex2		84	84									
Virtex2p		84	84									
VirtexE		84	84									
Virtex4		84	84									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		104										

M5_B1B16_SB**1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		84	84									
Stratix		84	84									
Stratix2		80	80									
Max2		84	84									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		84	84									
Spartan2E		84	84									
Spartan3		84	84									
Virtex		84	84									
Virtex2		84	84									
Virtex2p		84	84									
VirtexE		84	84									
Virtex4		84	84									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		104										

M5_B1B16E

1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		84	84									
Stratix		84	84									
Stratix2		82	82									
Max2		84	84									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		84	84									
Spartan2E		84	84									
Spartan3		84	84									
Virtex		84	84									
Virtex2		84	84									
Virtex2p		84	84									
VirtexE		84	84									
Virtex4		84	84									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		106										

M5_B1B16E_SB**1x5-Bit Bus to 16x5-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		84	84									
Stratix		84	84									
Stratix2		82	82									
Max2		84	84									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		84	84									
Spartan2E		84	84									
Spartan3		84	84									
Virtex		84	84									
Virtex2		84	84									
Virtex2p		84	84									
VirtexE		84	84									
Virtex4		84	84									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		106										

M5_B2B1

2x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		5	5									
Max2		5	5									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		5										

M5_B2B1E**2x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		5	5									
Max2		5	5									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		10										

M5_B4B1

4x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		24										

M5_B4B1_SB

4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		5	5									
Max2		10	10									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		24										

M5_B4B1E

4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		5	5									
Max2		15	15									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		16	15									
Spartan2E		16	15									
Spartan3		16	15									
Virtex		16	15									
Virtex2		16	15									
Virtex2p		16	15									
VirtexE		16	15									
Virtex4		16	15									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		29										

M5_B4B1E_SB**4x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		15	15									
Stratix		15	15									
Stratix2		5	5									
Max2		15	15									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		16	15									
Spartan2E		16	15									
Spartan3		16	15									
Virtex		16	15									
Virtex2		16	15									
Virtex2p		16	15									
VirtexE		16	15									
Virtex4		16	15									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		29										

M5_B8B1

8x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		25	25									
Stratix		25	25									
Stratix2		15	15									
Max2		25	25									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		62										

M5_B8B1_SB

8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		25	25									
Stratix		25	25									
Stratix2		15	15									
Max2		25	25									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		62										

M5_B8B1E

8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		25	25									
Stratix		25	25									
Stratix2		15	15									
Max2		25	25									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		26	25									
Spartan2E		26	25									
Spartan3		26	25									
Virtex		26	25									
Virtex2		26	25									
Virtex2p		26	25									
VirtexE		26	25									
Virtex4		26	25									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		67										

M5_B8B1E_SB**8x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		25	25									
Stratix		25	25									
Stratix2		15	15									
Max2		25	25									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		26	25									
Spartan2E		26	25									
Spartan3		26	25									
Virtex		26	25									
Virtex2		26	25									
Virtex2p		26	25									
VirtexE		26	25									
Virtex4		26	25									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		67										

M5_B16B1

16x5-Bit Bus to 1x5-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		25	25									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		46	45									
Spartan2E		46	45									
Spartan3		40	40									
Virtex		46	45									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		46	45									
Virtex4		40	40									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		129										

M5_B16B1_SB

16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		25	25									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		46	45									
Spartan2E		46	45									
Spartan3		40	40									
Virtex		46	45									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		46	45									
Virtex4		40	40									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		129										

M5_B16B1E

16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		55	55									
Stratix		55	55									
Stratix2		25	25									
Max2		55	55									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		54	53									
Spartan2E		54	53									
Spartan3		54	53									
Virtex		54	53									
Virtex2		54	53									
Virtex2p		54	53									
VirtexE		54	53									
Virtex4		54	53									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		129										

M5_B16B1E_SB**16x5-Bit Bus to 1x5-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		55	55									
Stratix		55	55									
Stratix2		25	25									
Max2		55	55									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		54	53									
Spartan2E		54	53									
Spartan3		54	53									
Virtex		54	53									
Virtex2		54	53									
Virtex2p		54	53									
VirtexE		54	53									
Virtex4		54	53									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		129										

M5_S1S2

1x5-Single Wire to 1x5-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		10										

M5_S1S2E**1x5-Single Wire to 1x5-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		12										

M5_S2S1

2x5-Single Wire to 2x5-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		5	5									
Max2		5	5									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		5										

M5_S2S1E**2x5-Single Wire to 2x5-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		5	5									
Stratix		5	5									
Stratix2		5	5									
Max2		5	5									
Max3000a	5	5										
Max7000b	5	5										
Max7000ae	5	5										
Max7000s	5	5										
Spartan2		6	5									
Spartan2E		6	5									
Spartan3		6	5									
Virtex		6	5									
Virtex2		6	5									
Virtex2p		6	5									
VirtexE		6	5									
Virtex4		6	5									
CoolRunner2	5											
CoolRunnerXpla3	5											
Xc9500	5											
Xc9500XL	5											
Xc9500XV	5											
ProAsicPlus		10										

M6_B1B2

1x6-Bit Bus to 2x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		12										

M6_B1B2E**1x6-Bit Bus to 2x6-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		14										

M6_B1B4

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		28										

M6_B1B4_SB**1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		28										

M6_B1B4E

1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		30										

M6_B1B4E_SB**1x6-Bit Bus to 4x6-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		30										

M6_B1B8

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		60										

M6_B1B8_SB**1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		60										

M6_B1B8E

1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		56	56									
Stratix		56	56									
Stratix2		48	48									
Max2		56	56									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		50	50									
Spartan2E		50	50									
Spartan3		50	50									
Virtex		50	50									
Virtex2		50	50									
Virtex2p		50	50									
VirtexE		50	50									
Virtex4		50	50									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		62										

M6_B1B8E_SB**1x6-Bit Bus to 8x6-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		56	56									
Stratix		56	56									
Stratix2		48	48									
Max2		56	56									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		50	50									
Spartan2E		50	50									
Spartan3		50	50									
Virtex		50	50									
Virtex2		50	50									
Virtex2p		50	50									
VirtexE		50	50									
Virtex4		50	50									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		62										

M6_B1B16

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		112	112									
Stratix		112	112									
Stratix2		96	96									
Max2		112	112									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		100	100									
Spartan2E		100	100									
Spartan3		100	100									
Virtex		100	100									
Virtex2		100	100									
Virtex2p		100	100									
VirtexE		100	100									
Virtex4		100	100									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		120										

M6_B1B16_SB**1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		112	112									
Stratix		112	112									
Stratix2		96	96									
Max2		112	112									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		100	100									
Spartan2E		100	100									
Spartan3		100	100									
Virtex		100	100									
Virtex2		100	100									
Virtex2p		100	100									
VirtexE		100	100									
Virtex4		100	100									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		120										

M6_B1B16E

1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		114	114									
Stratix		114	114									
Stratix2		98	98									
Max2		114	114									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		100	100									
Spartan2E		100	100									
Spartan3		100	100									
Virtex		100	100									
Virtex2		100	100									
Virtex2p		100	100									
VirtexE		100	100									
Virtex4		100	100									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		122										

M6_B1B16E_SB**1x6-Bit Bus to 16x6-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		114	114									
Stratix		114	114									
Stratix2		98	98									
Max2		114	114									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		100	100									
Spartan2E		100	100									
Spartan3		100	100									
Virtex		100	100									
Virtex2		100	100									
Virtex2p		100	100									
VirtexE		100	100									
Virtex4		100	100									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		122										

M6_B2B1

2x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		6										

M6_B2B1E

2x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		12										

M6_B4B1

4x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		6	6									
Max2		12	12									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		28										

M6_B4B1_SB

4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		6	6									
Max2		12	12									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		28										

M6_B4B1E

4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		6	6									
Max2		18	18									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		34										

M6_B4B1E_SB**4x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		6	6									
Max2		18	18									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		34										

M6_B8B1

8x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		18	18									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		72										

M6_B8B1_SB

8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		18	18									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		72										

M6_B8B1E

8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		18	18									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		30	30									
Spartan2E		30	30									
Spartan3		30	30									
Virtex		30	30									
Virtex2		30	30									
Virtex2p		30	30									
VirtexE		30	30									
Virtex4		30	30									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		78										

M6_B8B1E_SB**8x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		18	18									
Max2		30	30									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		30	30									
Spartan2E		30	30									
Spartan3		30	30									
Virtex		30	30									
Virtex2		30	30									
Virtex2p		30	30									
VirtexE		30	30									
Virtex4		30	30									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		78										

M6_B16B1

16x6-Bit Bus to 1x6-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		30	30									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		54	54									
Spartan2E		54	54									
Spartan3		48	48									
Virtex		54	54									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		54	54									
Virtex4		48	48									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		150										

M6_B16B1_SB**16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		30	30									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		54	54									
Spartan2E		54	54									
Spartan3		48	48									
Virtex		54	54									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		54	54									
Virtex4		48	48									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		150										

M6_B16B1E

16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		66	66									
Stratix		66	66									
Stratix2		30	30									
Max2		66	66									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		62	62									
Spartan2E		62	62									
Spartan3		62	62									
Virtex		62	62									
Virtex2		62	62									
Virtex2p		62	62									
VirtexE		62	62									
Virtex4		62	62									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		150										

M6_B16B1E_SB**16x6-Bit Bus to 1x6-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		66	66									
Stratix		66	66									
Stratix2		30	30									
Max2		66	66									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		62	62									
Spartan2E		62	62									
Spartan3		64	63									
Virtex		62	62									
Virtex2		64	63									
Virtex2p		64	63									
VirtexE		62	62									
Virtex4		64	63									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		150										

M6_S1S2

1x6-Single Wire to 1x6-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		12										

M6_S1S2E**1x6-Single Wire to 1x6-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		14										

M6_S2S1

2x6-Single Wire to 2x6-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		6										

M6_S2S1E

2x6-Single Wire to 2x6-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		6	6									
Stratix		6	6									
Stratix2		6	6									
Max2		6	6									
Max3000a	6	6										
Max7000b	6	6										
Max7000ae	6	6										
Max7000s	6	6										
Spartan2		6	6									
Spartan2E		6	6									
Spartan3		6	6									
Virtex		6	6									
Virtex2		6	6									
Virtex2p		6	6									
VirtexE		6	6									
Virtex4		6	6									
CoolRunner2	6											
CoolRunnerXpla3	6											
Xc9500	6											
Xc9500XL	6											
Xc9500XV	6											
ProAsicPlus		12										

M7_B1B2

1x7-Bit Bus to 2x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		14	14									
Max2		14	14									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	14											
CoolRunnerXpla3	14											
Xc9500	14											
Xc9500XL	14											
Xc9500XV	14											
ProAsicPlus		14										

M7_B1B2E**1x7-Bit Bus to 2x7-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		14	14									
Max2		14	14									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	14											
CoolRunnerXpla3	14											
Xc9500	14											
Xc9500XL	14											
Xc9500XV	14											
ProAsicPlus		16										

M7_B1B4

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		28	28									
Stratix		28	28									
Stratix2		28	28									
Max2		28	28									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	28											
CoolRunnerXpla3	28											
Xc9500	28											
Xc9500XL	28											
Xc9500XV	28											
ProAsicPlus		32										

M7_B1B4_SB**1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		28	28									
Stratix		28	28									
Stratix2		28	28									
Max2		28	28									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	28											
CoolRunnerXpla3	28											
Xc9500	28											
Xc9500XL	28											
Xc9500XV	28											
ProAsicPlus		32										

M7_B1B4E

1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		28	28									
Stratix		28	28									
Stratix2		28	28									
Max2		28	28									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	28											
CoolRunnerXpla3	28											
Xc9500	28											
Xc9500XL	28											
Xc9500XV	28											
ProAsicPlus		34										

M7_B1B4E_SB**1x7-Bit Bus to 4x7-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		28	28									
Stratix		28	28									
Stratix2		28	28									
Max2		28	28									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	28											
CoolRunnerXpla3	28											
Xc9500	28											
Xc9500XL	28											
Xc9500XV	28											
ProAsicPlus		34										

M7_B1B8

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		56	56									
Stratix		56	56									
Stratix2		56	56									
Max2		56	56									
Max3000a	56	56										
Max7000b	56	56										
Max7000ae	56	56										
Max7000s	56	56										
Spartan2		56	56									
Spartan2E		56	56									
Spartan3		56	56									
Virtex		56	56									
Virtex2		56	56									
Virtex2p		56	56									
VirtexE		56	56									
Virtex4		56	56									
CoolRunner2	56											
CoolRunnerXpla3	56											
Xc9500	56											
Xc9500XL	56											
Xc9500XV	56											
ProAsicPlus		68										

M7_B1B8_SB**1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		56	56									
Stratix		56	56									
Stratix2		56	56									
Max2		56	56									
Max3000a	56	56										
Max7000b	56	56										
Max7000ae	56	56										
Max7000s	56	56										
Spartan2		56	56									
Spartan2E		56	56									
Spartan3		56	56									
Virtex		56	56									
Virtex2		56	56									
Virtex2p		56	56									
VirtexE		56	56									
Virtex4		56	56									
CoolRunner2	56											
CoolRunnerXpla3	56											
Xc9500	56											
Xc9500XL	56											
Xc9500XV	56											
ProAsicPlus		68										

M7_B1B8E

1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		56	56									
Max2		64	64									
Max3000a	56	56										
Max7000b	56	56										
Max7000ae	56	56										
Max7000s	56	56										
Spartan2		58	58									
Spartan2E		58	58									
Spartan3		58	58									
Virtex		58	58									
Virtex2		58	58									
Virtex2p		58	58									
VirtexE		58	58									
Virtex4		58	58									
CoolRunner2	56											
CoolRunnerXpla3	56											
Xc9500	56											
Xc9500XL	56											
Xc9500XV	56											
ProAsicPlus		70										

M7_B1B8E_SB**1x7-Bit Bus to 8x7-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		56	56									
Max2		64	64									
Max3000a	56	56										
Max7000b	56	56										
Max7000ae	56	56										
Max7000s	56	56										
Spartan2		58	58									
Spartan2E		58	58									
Spartan3		58	58									
Virtex		58	58									
Virtex2		58	58									
Virtex2p		58	58									
VirtexE		58	58									
Virtex4		58	58									
CoolRunner2	56											
CoolRunnerXpla3	56											
Xc9500	56											
Xc9500XL	56											
Xc9500XV	56											
ProAsicPlus		70										

M7_B1B16

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		112	112									
Max2		128	128									
Max3000a	112	112										
Max7000b	112	112										
Max7000ae	112	112										
Max7000s	112	112										
Spartan2		120	120									
Spartan2E		120	120									
Spartan3		120	120									
Virtex		120	120									
Virtex2		120	120									
Virtex2p		120	120									
VirtexE		120	120									
Virtex4		120	120									
CoolRunner2	112											
CoolRunnerXpla3	112											
Xc9500	112											
Xc9500XL	112											
Xc9500XV	112											
ProAsicPlus		136										

M7_B1B16_SB**1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		112	112									
Max2		128	128									
Max3000a	112	112										
Max7000b	112	112										
Max7000ae	112	112										
Max7000s	112	112										
Spartan2		120	120									
Spartan2E		120	120									
Spartan3		120	120									
Virtex		120	120									
Virtex2		120	120									
Virtex2p		120	120									
VirtexE		120	120									
Virtex4		120	120									
CoolRunner2	112											
CoolRunnerXpla3	112											
Xc9500	112											
Xc9500XL	112											
Xc9500XV	112											
ProAsicPlus		136										

M7_B1B16E

1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		130	130									
Stratix		130	130									
Stratix2		114	114									
Max2		130	130									
Max3000a	112	112										
Max7000b	112	112										
Max7000ae	112	112										
Max7000s	112	112										
Spartan2		120	120									
Spartan2E		120	120									
Spartan3		120	120									
Virtex		120	120									
Virtex2		120	120									
Virtex2p		120	120									
VirtexE		120	120									
Virtex4		120	120									
CoolRunner2	112											
CoolRunnerXpla3	112											
Xc9500	112											
Xc9500XL	112											
Xc9500XV	112											
ProAsicPlus		138										

M7_B1B16E_SB**1x7-Bit Bus to 16x7-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		130	130									
Stratix		130	130									
Stratix2		114	114									
Max2		130	130									
Max3000a	112	112										
Max7000b	112	112										
Max7000ae	112	112										
Max7000s	112	112										
Spartan2		122	120									
Spartan2E		122	120									
Spartan3		122	120									
Virtex		122	120									
Virtex2		122	120									
Virtex2p		122	120									
VirtexE		122	120									
Virtex4		122	120									
CoolRunner2	112											
CoolRunnerXpla3	112											
Xc9500	112											
Xc9500XL	112											
Xc9500XV	112											
ProAsicPlus		138										

M7_B2B1

2x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		7	7									
Stratix		7	7									
Stratix2		7	7									
Max2		7	7									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		8	7									
Spartan2E		8	7									
Spartan3		8	7									
Virtex		8	7									
Virtex2		8	7									
Virtex2p		8	7									
VirtexE		8	7									
Virtex4		8	7									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		7										

M7_B2B1E**2x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		7	7									
Stratix		7	7									
Stratix2		7	7									
Max2		7	7									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		8	7									
Spartan2E		8	7									
Spartan3		8	7									
Virtex		8	7									
Virtex2		8	7									
Virtex2p		8	7									
VirtexE		8	7									
Virtex4		8	7									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		14										

M7_B4B1

4x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		7	7									
Max2		14	14									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		32										

M7_B4B1_SB

4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		7	7									
Max2		14	14									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		32										

M7_B4B1E

4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		21	21									
Stratix		21	21									
Stratix2		7	7									
Max2		21	21									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		22	21									
Spartan2E		22	21									
Spartan3		22	21									
Virtex		22	21									
Virtex2		22	21									
Virtex2p		22	21									
VirtexE		22	21									
Virtex4		22	21									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		39										

M7_B4B1E_SB**4x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		21	21									
Stratix		21	21									
Stratix2		7	7									
Max2		21	21									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		22	21									
Spartan2E		22	21									
Spartan3		22	21									
Virtex		22	21									
Virtex2		22	21									
Virtex2p		22	21									
VirtexE		22	21									
Virtex4		22	21									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		39										

M7_B8B1

8x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		35	35									
Stratix		35	35									
Stratix2		21	21									
Max2		35	35									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		82										

M7_B8B1_SB

8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		35	35									
Stratix		35	35									
Stratix2		21	21									
Max2		35	35									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		28	28									
Spartan2E		28	28									
Spartan3		28	28									
Virtex		28	28									
Virtex2		28	28									
Virtex2p		28	28									
VirtexE		28	28									
Virtex4		28	28									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		82										

M7_B8B1E

8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		35	35									
Stratix		35	35									
Stratix2		21	21									
Max2		35	35									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		36	35									
Spartan2E		36	35									
Spartan3		36	35									
Virtex		36	35									
Virtex2		36	35									
Virtex2p		36	35									
VirtexE		36	35									
Virtex4		36	35									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		89										

M7_B8B1E_SB**8x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		35	35									
Stratix		35	35									
Stratix2		21	21									
Max2		35	35									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		36	35									
Spartan2E		36	35									
Spartan3		36	35									
Virtex		36	35									
Virtex2		36	35									
Virtex2p		36	35									
VirtexE		36	35									
Virtex4		36	35									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		89										

M7_B16B1

16x7-Bit Bus to 1x7-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		70	70									
Stratix		70	70									
Stratix2		35	35									
Max2		70	70									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		64	63									
Spartan2E		64	63									
Spartan3		56	56									
Virtex		64	63									
Virtex2		56	56									
Virtex2p		56	56									
VirtexE		64	63									
Virtex4		56	56									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		171										

M7_B16B1_SB**16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		70	70									
Stratix		70	70									
Stratix2		35	35									
Max2		70	70									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		64	63									
Spartan2E		64	63									
Spartan3		56	56									
Virtex		64	63									
Virtex2		56	56									
Virtex2p		56	56									
VirtexE		64	63									
Virtex4		56	56									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		171										

M7_B16B1E

16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		77	77									
Stratix		77	77									
Stratix2		35	35									
Max2		77	77									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		72	72									
Virtex		72	72									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		72	72									
Virtex4		72	72									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		171										

M7_B16B1E_SB**16x7-Bit Bus to 1x7-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		77	77									
Stratix		77	77									
Stratix2		35	35									
Max2		77	77									
Max3000a	28	28										
Max7000b	28	28										
Max7000ae	28	28										
Max7000s	28	28										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		72	72									
Virtex		72	72									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		72	72									
Virtex4		72	72									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		171										

M7_S1S2

1x7-Single Wire to 1x7-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		14	14									
Max2		14	14									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	14											
CoolRunnerXpla3	14											
Xc9500	14											
Xc9500XL	14											
Xc9500XV	14											
ProAsicPlus		14										

M7_S1S2E**1x7-Single Wire to 1x7-Single Wire Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		14	14									
Stratix		14	14									
Stratix2		14	14									
Max2		14	14									
Max3000a	14	14										
Max7000b	14	14										
Max7000ae	14	14										
Max7000s	14	14										
Spartan2		14	14									
Spartan2E		14	14									
Spartan3		14	14									
Virtex		14	14									
Virtex2		14	14									
Virtex2p		14	14									
VirtexE		14	14									
Virtex4		14	14									
CoolRunner2	14											
CoolRunnerXpla3	14											
Xc9500	14											
Xc9500XL	14											
Xc9500XV	14											
ProAsicPlus		16										

M7_S2S1

2x7-Single Wire to 2x7-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		7	7									
Stratix		7	7									
Stratix2		7	7									
Max2		7	7									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		8	7									
Spartan2E		8	7									
Spartan3		8	7									
Virtex		8	7									
Virtex2		8	7									
Virtex2p		8	7									
VirtexE		8	7									
Virtex4		8	7									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		7										

M7_S2S1E**2x7-Single Wire to 2x7-Single Wire Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		7	7									
Stratix		7	7									
Stratix2		7	7									
Max2		7	7									
Max3000a	7	7										
Max7000b	7	7										
Max7000ae	7	7										
Max7000s	7	7										
Spartan2		8	7									
Spartan2E		8	7									
Spartan3		8	7									
Virtex		8	7									
Virtex2		8	7									
Virtex2p		8	7									
VirtexE		8	7									
Virtex4		8	7									
CoolRunner2	7											
CoolRunnerXpla3	7											
Xc9500	7											
Xc9500XL	7											
Xc9500XV	7											
ProAsicPlus		14										

M8_B1B2

1x8-Bit Bus to 2x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		16										

M8_B1B2E**1x8-Bit Bus to 2x8-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		18										

M8_B1B4

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		36										

M8_B1B4_SB**1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		36										

M8_B1B4E

1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		38										

M8_B1B4E_SB**1x8-Bit Bus to 4x8-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		38										

M8_B1B8

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		76										

M8_B1B8_SB**1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		76										

M8_B1B8E

1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		72	72									
Stratix		72	72									
Stratix2		64	64									
Max2		72	72									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		66	66									
Spartan2E		66	66									
Spartan3		66	66									
Virtex		66	66									
Virtex2		66	66									
Virtex2p		66	66									
VirtexE		66	66									
Virtex4		66	66									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		78										

M8_B1B8E_SB**1x8-Bit Bus to 8x8-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		72	72									
Stratix		72	72									
Stratix2		64	64									
Max2		72	72									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		66	66									
Spartan2E		66	66									
Spartan3		66	66									
Virtex		66	66									
Virtex2		66	66									
Virtex2p		66	66									
VirtexE		66	66									
Virtex4		66	66									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		78										

M8_B1B16

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		144	144									
Stratix		144	144									
Stratix2		128	128									
Max2		144	144									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		136	136									
Spartan2E		136	136									
Spartan3		136	136									
Virtex		136	136									
Virtex2		136	136									
Virtex2p		136	136									
VirtexE		136	136									
Virtex4		136	136									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		152										

M8_B1B16_SB**1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		144	144									
Stratix		144	144									
Stratix2		128	128									
Max2		144	144									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		136	136									
Spartan2E		136	136									
Spartan3		136	136									
Virtex		136	136									
Virtex2		136	136									
Virtex2p		136	136									
VirtexE		136	136									
Virtex4		136	136									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		152										

M8_B1B16E

1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		146	146									
Stratix		146	146									
Stratix2		130	130									
Max2		146	146									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		136	136									
Spartan2E		136	136									
Spartan3		136	136									
Virtex		136	136									
Virtex2		136	136									
Virtex2p		136	136									
VirtexE		136	136									
Virtex4		136	136									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		154										

M8_B1B16E_SB**1x8-Bit Bus to 16x8-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		146	146									
Stratix		146	146									
Stratix2		130	130									
Max2		146	146									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		138	136									
Spartan2E		138	136									
Spartan3		138	136									
Virtex		138	136									
Virtex2		138	136									
Virtex2p		138	136									
VirtexE		138	136									
Virtex4		138	136									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		154										

M8_B2B1

2x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		8										

M8_B2B1E**2x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		16										

M8_B4B1

4x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		8	8									
Max2		16	16									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		36										

M8_B4B1_SB

4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		8	8									
Max2		16	16									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		36										

M8_B4B1E

4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		8	8									
Max2		24	24									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		44										

M8_B4B1E_SB**4x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		8	8									
Max2		24	24									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		44										

M8_B8B1

8x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		24	24									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		92										

M8_B8B1_SB

8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		24	24									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		92										

M8_B8B1E

8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		24	24									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		100										

M8_B8B1E_SB**8x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		24	24									
Max2		40	40									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		100										

M8_B16B1

16x8-Bit Bus to 1x8-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		40	40									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		64	64									
Virtex		72	72									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		72	72									
Virtex4		64	64									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		192										

M8_B16B1_SB**16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		40	40									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		64	64									
Virtex		72	72									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		72	72									
Virtex4		64	64									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		192										

M8_B16B1E

16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		88	88									
Stratix		88	88									
Stratix2		40	40									
Max2		88	88									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		82	82									
Spartan2E		82	82									
Spartan3		82	82									
Virtex		82	82									
Virtex2		82	82									
Virtex2p		82	82									
VirtexE		82	82									
Virtex4		82	82									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		192										

M8_B16B1E_SB**16x8-Bit Bus to 1x8-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		88	88									
Stratix		88	88									
Stratix2		40	40									
Max2		88	88									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		82	82									
Spartan2E		82	82									
Spartan3		82	82									
Virtex		82	82									
Virtex2		82	82									
Virtex2p		82	82									
VirtexE		82	82									
Virtex4		82	82									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		192										

M8_S1S2

1x8-Single Wire to 2x8-Single Wire Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		16										

M8_S1S2E**1x8-Single Wire to 2x8-Single Wire Multiplexer (Demultiplex)
With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		18										

M8_S2S1

2x8-Single Wire to 1x8-Single Wire Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		8										

M8_S2S1E

2x8-Single Wire to 1x8-Single Wire Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		8	8									
Stratix		8	8									
Stratix2		8	8									
Max2		8	8									
Max3000a	8	8										
Max7000b	8	8										
Max7000ae	8	8										
Max7000s	8	8										
Spartan2		8	8									
Spartan2E		8	8									
Spartan3		8	8									
Virtex		8	8									
Virtex2		8	8									
Virtex2p		8	8									
VirtexE		8	8									
Virtex4		8	8									
CoolRunner2	8											
CoolRunnerXpla3	8											
Xc9500	8											
Xc9500XL	8											
Xc9500XV	8											
ProAsicPlus		16										

M9_B1B2

1x9-Bit Bus to 2x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		18	18									
Max2		18	18									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	18											
CoolRunnerXpla3	18											
Xc9500	18											
Xc9500XL	18											
Xc9500XV	18											
ProAsicPlus		18										

M9_B1B2E**1x9-Bit Bus to 2x9-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		18	18									
Max2		18	18									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	18											
CoolRunnerXpla3	18											
Xc9500	18											
Xc9500XL	18											
Xc9500XV	18											
ProAsicPlus		20										

M9_B1B4

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		36	36									
Max2		36	36									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	36											
CoolRunnerXpla3	36											
Xc9500	36											
Xc9500XL	36											
Xc9500XV	36											
ProAsicPlus		40										

M9_B1B4_SB**1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		36	36									
Max2		36	36									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	36											
CoolRunnerXpla3	36											
Xc9500	36											
Xc9500XL	36											
Xc9500XV	36											
ProAsicPlus		40										

M9_B1B4E

1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		36	36									
Max2		36	36									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	36											
CoolRunnerXpla3	36											
Xc9500	36											
Xc9500XL	36											
Xc9500XV	36											
ProAsicPlus		42										

M9_B1B4E_SB**1x9-Bit Bus to 4x9-Bit Bus Multiplexer (Demultiplex) With Enable
With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		36	36									
Max2		36	36									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	36											
CoolRunnerXpla3	36											
Xc9500	36											
Xc9500XL	36											
Xc9500XV	36											
ProAsicPlus		42										

M9_B1B8

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		72	72									
Stratix		72	72									
Stratix2		72	72									
Max2		72	72									
Max3000a	72	72										
Max7000b	72	72										
Max7000ae	72	72										
Max7000s	72	72										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		72	72									
Virtex		72	72									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		72	72									
Virtex4		72	72									
CoolRunner2	72											
CoolRunnerXpla3	72											
Xc9500	72											
Xc9500XL	72											
Xc9500XV	72											
ProAsicPlus		84										

M9_B1B8_SB**1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		72	72									
Stratix		72	72									
Stratix2		72	72									
Max2		72	72									
Max3000a	72	72										
Max7000b	72	72										
Max7000ae	72	72										
Max7000s	72	72										
Spartan2		72	72									
Spartan2E		72	72									
Spartan3		72	72									
Virtex		72	72									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		72	72									
Virtex4		72	72									
CoolRunner2	72											
CoolRunnerXpla3	72											
Xc9500	72											
Xc9500XL	72											
Xc9500XV	72											
ProAsicPlus		84										

M9_B1B8E

1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		72	72									
Max2		80	80									
Max3000a	72	72										
Max7000b	72	72										
Max7000ae	72	72										
Max7000s	72	72										
Spartan2		74	74									
Spartan2E		74	74									
Spartan3		74	74									
Virtex		74	74									
Virtex2		74	74									
Virtex2p		74	74									
VirtexE		74	74									
Virtex4		74	74									
CoolRunner2	72											
CoolRunnerXpla3	72											
Xc9500	72											
Xc9500XL	72											
Xc9500XV	72											
ProAsicPlus		86										

M9_B1B8E_SB**1x9-Bit Bus to 8x9-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		72	72									
Max2		80	80									
Max3000a	72	72										
Max7000b	72	72										
Max7000ae	72	72										
Max7000s	72	72										
Spartan2		74	74									
Spartan2E		74	74									
Spartan3		74	74									
Virtex		74	74									
Virtex2		74	74									
Virtex2p		74	74									
VirtexE		74	74									
Virtex4		74	74									
CoolRunner2	72											
CoolRunnerXpla3	72											
Xc9500	72											
Xc9500XL	72											
Xc9500XV	72											
ProAsicPlus		86										

M9_B1B16

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		144	144									
Max2		160	160									
Max3000a	144	144										
Max7000b	144	144										
Max7000ae	144	144										
Max7000s	144	144										
Spartan2		154	152									
Spartan2E		154	152									
Spartan3		154	152									
Virtex		154	152									
Virtex2		154	152									
Virtex2p		154	152									
VirtexE		154	152									
Virtex4		152	152									
CoolRunner2	144											
CoolRunnerXpla3	144											
Xc9500	144											
Xc9500XL	144											
Xc9500XV	144											
ProAsicPlus		168										

M9_B1B16_SB**1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		144	144									
Max2		160	160									
Max3000a	144	144										
Max7000b	144	144										
Max7000ae	144	144										
Max7000s	144	144										
Spartan2		152	152									
Spartan2E		152	152									
Spartan3		152	152									
Virtex		152	152									
Virtex2		152	152									
Virtex2p		152	152									
VirtexE		152	152									
Virtex4		152	152									
CoolRunner2	144											
CoolRunnerXpla3	144											
Xc9500	144											
Xc9500XL	144											
Xc9500XV	144											
ProAsicPlus		168										

M9_B1B16E

1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		162	162									
Stratix		162	162									
Stratix2		146	146									
Max2		162	162									
Max3000a	144	144										
Max7000b	144	144										
Max7000ae	144	144										
Max7000s	144	144										
Spartan2		152	152									
Spartan2E		152	152									
Spartan3		152	152									
Virtex		152	152									
Virtex2		152	152									
Virtex2p		152	152									
VirtexE		152	152									
Virtex4		152	152									
CoolRunner2	144											
CoolRunnerXpla3	144											
Xc9500	144											
Xc9500XL	144											
Xc9500XV	144											
ProAsicPlus		170										

M9_B1B16E_SB**1x9-Bit Bus to 16x9-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		162	162									
Stratix		162	162									
Stratix2		146	146									
Max2		162	162									
Max3000a	144	144										
Max7000b	144	144										
Max7000ae	144	144										
Max7000s	144	144										
Spartan2		154	152									
Spartan2E		154	152									
Spartan3		154	152									
Virtex		154	152									
Virtex2		154	152									
Virtex2p		154	152									
VirtexE		154	152									
Virtex4		152	152									
CoolRunner2	144											
CoolRunnerXpla3	144											
Xc9500	144											
Xc9500XL	144											
Xc9500XV	144											
ProAsicPlus		170										

M9_B2B1

2x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		9	9									
Max2		9	9									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		9										

M9_B2B1E

2x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		9	9									
Stratix		9	9									
Stratix2		9	9									
Max2		9	9									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		10	9									
Spartan2E		10	9									
Spartan3		10	9									
Virtex		10	9									
Virtex2		10	9									
Virtex2p		10	9									
VirtexE		10	9									
Virtex4		10	9									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		18										

M9_B4B1

4x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		9	9									
Max2		18	18									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		40										

M9_B4B1_SB

4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		18	18									
Stratix		18	18									
Stratix2		9	9									
Max2		18	18									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		18	18									
Spartan2E		18	18									
Spartan3		18	18									
Virtex		18	18									
Virtex2		18	18									
Virtex2p		18	18									
VirtexE		18	18									
Virtex4		18	18									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		40										

M9_B4B1E

4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		27	27									
Stratix		27	27									
Stratix2		9	9									
Max2		27	27									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		28	27									
Spartan2E		28	27									
Spartan3		28	27									
Virtex		28	27									
Virtex2		28	27									
Virtex2p		28	27									
VirtexE		28	27									
Virtex4		28	27									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		49										

M9_B4B1E_SB**4x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		27	27									
Stratix		27	27									
Stratix2		9	9									
Max2		27	27									
Max3000a	9	9										
Max7000b	9	9										
Max7000ae	9	9										
Max7000s	9	9										
Spartan2		28	27									
Spartan2E		28	27									
Spartan3		28	27									
Virtex		28	27									
Virtex2		28	27									
Virtex2p		28	27									
VirtexE		28	27									
Virtex4		28	27									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		49										

M9_B8B1

8x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		45	45									
Stratix		45	45									
Stratix2		27	27									
Max2		45	45									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		102										

M9_B8B1_SB

8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		45	45									
Stratix		45	45									
Stratix2		27	27									
Max2		45	45									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		102										

M9_B8B1E

8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		45	45									
Stratix		45	45									
Stratix2		27	27									
Max2		45	45									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		46	45									
Spartan2E		46	45									
Spartan3		46	45									
Virtex		46	45									
Virtex2		46	45									
Virtex2p		46	45									
VirtexE		46	45									
Virtex4		46	45									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		111										

M9_B8B1E_SB**8x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		45	45									
Stratix		45	45									
Stratix2		27	27									
Max2		45	45									
Max3000a	18	18										
Max7000b	18	18										
Max7000ae	18	18										
Max7000s	18	18										
Spartan2		46	45									
Spartan2E		46	45									
Spartan3		46	45									
Virtex		46	45									
Virtex2		46	45									
Virtex2p		46	45									
VirtexE		46	45									
Virtex4		46	45									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		111										

M9_B16B1

16x9-Bit Bus to 1x9-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		90	90									
Stratix		90	90									
Stratix2		45	45									
Max2		90	90									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		82	81									
Spartan2E		82	81									
Spartan3		72	72									
Virtex		82	81									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		82	81									
Virtex4		72	72									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		213										

M9_B16B1_SB

16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		90	90									
Stratix		90	90									
Stratix2		45	45									
Max2		90	90									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		82	81									
Spartan2E		82	81									
Spartan3		72	72									
Virtex		82	81									
Virtex2		72	72									
Virtex2p		72	72									
VirtexE		82	81									
Virtex4		72	72									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		213										

M9_B16B1E

16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		99	99									
Stratix		99	99									
Stratix2		45	45									
Max2		99	99									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		84	83									
Spartan2E		84	83									
Spartan3		84	83									
Virtex		84	83									
Virtex2		84	83									
Virtex2p		84	83									
VirtexE		84	83									
Virtex4		84	83									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		213										

M9_B16B1E_SB**16x9-Bit Bus to 1x9-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		99	99									
Stratix		99	99									
Stratix2		45	45									
Max2		99	99									
Max3000a	36	36										
Max7000b	36	36										
Max7000ae	36	36										
Max7000s	36	36										
Spartan2		84	83									
Spartan2E		84	83									
Spartan3		84	83									
Virtex		84	83									
Virtex2		84	83									
Virtex2p		84	83									
VirtexE		84	83									
Virtex4		84	83									
CoolRunner2	9											
CoolRunnerXpla3	9											
Xc9500	9											
Xc9500XL	9											
Xc9500XV	9											
ProAsicPlus		213										

M10_B1B2

1x10-Bit Bus to 2x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		20	20									
Max2		20	20									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	20											
CoolRunnerXpla3	20											
Xc9500	20											
Xc9500XL	20											
Xc9500XV	20											
ProAsicPlus		20										

M10_B1B2E**1x10-Bit Bus to 2x10-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements		4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20										
Stratix		20	20										
Stratix2		20	20										
Max2		20	20										
Max3000a	20	20											
Max7000b	20	20											
Max7000ae	20	20											
Max7000s	20	20											
Spartan2		20	20										
Spartan2E		20	20										
Spartan3		20	20										
Virtex		20	20										
Virtex2		20	20										
Virtex2p		20	20										
VirtexE		20	20										
Virtex4		20	20										
CoolRunner2	20												
CoolRunnerXpla3	20												
Xc9500	20												
Xc9500XL	20												
Xc9500XV	20												
ProAsicPlus		22											

M10_B1B4

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		44										

M10_B1B4_SB**1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		44										

M10_B1B4E

1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		46										

M10_B1B4E_SB**1x10-Bit Bus to 4x10-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		40	40									
Stratix		40	40									
Stratix2		40	40									
Max2		40	40									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	40											
CoolRunnerXpla3	40											
Xc9500	40											
Xc9500XL	40											
Xc9500XV	40											
ProAsicPlus		46										

M10_B1B8

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		80	80									
Max2		80	80									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		80	80									
Spartan2E		80	80									
Spartan3		80	80									
Virtex		80	80									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		80	80									
Virtex4		80	80									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		92										

M10_B1B8_SB**1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		80	80									
Max2		80	80									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		80	80									
Spartan2E		80	80									
Spartan3		80	80									
Virtex		80	80									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		80	80									
Virtex4		80	80									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		92										

M10_B1B8E

1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		88	88									
Stratix		88	88									
Stratix2		80	80									
Max2		88	88									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		82	82									
Spartan2E		82	82									
Spartan3		82	82									
Virtex		82	82									
Virtex2		82	82									
Virtex2p		82	82									
VirtexE		82	82									
Virtex4		82	82									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		94										

M10_B1B8E_SB**1x10-Bit Bus to 8x10-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		88	88									
Stratix		88	88									
Stratix2		80	80									
Max2		88	88									
Max3000a	80	80										
Max7000b	80	80										
Max7000ae	80	80										
Max7000s	80	80										
Spartan2		82	82									
Spartan2E		82	82									
Spartan3		82	82									
Virtex		82	82									
Virtex2		82	82									
Virtex2p		82	82									
VirtexE		82	82									
Virtex4		82	82									
CoolRunner2	80											
CoolRunnerXpla3	80											
Xc9500	80											
Xc9500XL	80											
Xc9500XV	80											
ProAsicPlus		94										

M10_B1B16

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		176	176									
Stratix		176	176									
Stratix2		160	160									
Max2		176	176									
Max3000a	160	160										
Max7000b	160	160										
Max7000ae	160	160										
Max7000s	160	160										
Spartan2		168	168									
Spartan2E		168	168									
Spartan3		168	168									
Virtex		168	168									
Virtex2		168	168									
Virtex2p		168	168									
VirtexE		168	168									
Virtex4		170	168									
CoolRunner2	160											
CoolRunnerXpla3	160											
ProAsicPlus		184										

M10_B1B16_SB**1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		176	176									
Stratix		176	176									
Stratix2		160	160									
Max2		176	176									
Max3000a	160	160										
Max7000b	160	160										
Max7000ae	160	160										
Max7000s	160	160										
Spartan2		168	168									
Spartan2E		168	168									
Spartan3		168	168									
Virtex		168	168									
Virtex2		168	168									
Virtex2p		168	168									
VirtexE		168	168									
Virtex4		168	168									
CoolRunner2	160											
CoolRunnerXpla3	160											
ProAsicPlus		184										

M10_B1B16E

1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		178	178									
Stratix		178	178									
Stratix2		162	162									
Max2		178	178									
Max3000a	160	160										
Max7000b	160	160										
Max7000ae	160	160										
Max7000s	160	160										
Spartan2		168	168									
Spartan2E		168	168									
Spartan3		168	168									
Virtex		168	168									
Virtex2		168	168									
Virtex2p		168	168									
VirtexE		168	168									
Virtex4		168	168									
CoolRunner2	160											
CoolRunnerXpla3	160											
ProAsicPlus		186										

M10_B1B16E_SB**1x10-Bit Bus to 16x10-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		178	178									
Stratix		178	178									
Stratix2		162	162									
Max2		178	178									
Max3000a	160	160										
Max7000b	160	160										
Max7000ae	160	160										
Max7000s	160	160										
Spartan2		168	168									
Spartan2E		168	168									
Spartan3		168	168									
Virtex		168	168									
Virtex2		168	168									
Virtex2p		168	168									
VirtexE		168	168									
Virtex4		168	168									
CoolRunner2	160											
CoolRunnerXpla3	160											
ProAsicPlus		186										

M10_B2B1

2x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		10										

M10_B2B1E**2x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		10	10									
Stratix		10	10									
Stratix2		10	10									
Max2		10	10									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		10	10									
Spartan2E		10	10									
Spartan3		10	10									
Virtex		10	10									
Virtex2		10	10									
Virtex2p		10	10									
VirtexE		10	10									
Virtex4		10	10									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		20										

M10_B4B1

4x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		10	10									
Max2		20	20									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		44										

M10_B4B1_SB

4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		20	20									
Stratix		20	20									
Stratix2		10	10									
Max2		20	20									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		20	20									
Spartan2E		20	20									
Spartan3		20	20									
Virtex		20	20									
Virtex2		20	20									
Virtex2p		20	20									
VirtexE		20	20									
Virtex4		20	20									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		44										

M10_B4B1E

4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		10	10									
Max2		30	30									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		30	30									
Spartan2E		30	30									
Spartan3		30	30									
Virtex		30	30									
Virtex2		30	30									
Virtex2p		30	30									
VirtexE		30	30									
Virtex4		30	30									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		54										

M10_B4B1E_SB**4x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		30	30									
Stratix		30	30									
Stratix2		10	10									
Max2		30	30									
Max3000a	10	10										
Max7000b	10	10										
Max7000ae	10	10										
Max7000s	10	10										
Spartan2		30	30									
Spartan2E		30	30									
Spartan3		30	30									
Virtex		30	30									
Virtex2		30	30									
Virtex2p		30	30									
VirtexE		30	30									
Virtex4		30	30									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		54										

M10_B8B1

8x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		30	30									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		112										

M10_B8B1_SB**8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		30	30									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		40	40									
Spartan2E		40	40									
Spartan3		40	40									
Virtex		40	40									
Virtex2		40	40									
Virtex2p		40	40									
VirtexE		40	40									
Virtex4		40	40									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		112										

M10_B8B1E

8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		30	30									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		50	50									
Spartan2E		50	50									
Spartan3		50	50									
Virtex		50	50									
Virtex2		50	50									
Virtex2p		50	50									
VirtexE		50	50									
Virtex4		50	50									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		122										

M10_B8B1E_SB**8x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		50	50									
Stratix		50	50									
Stratix2		30	30									
Max2		50	50									
Max3000a	20	20										
Max7000b	20	20										
Max7000ae	20	20										
Max7000s	20	20										
Spartan2		50	50									
Spartan2E		50	50									
Spartan3		50	50									
Virtex		50	50									
Virtex2		50	50									
Virtex2p		50	50									
VirtexE		50	50									
Virtex4		50	50									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		122										

M10_B16B1

16x10-Bit Bus to 1x10-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		100	100									
Stratix		100	100									
Stratix2		50	50									
Max2		100	100									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		90	90									
Spartan2E		90	90									
Spartan3		80	80									
Virtex		90	90									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		90	90									
Virtex4		80	80									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		234										

M10_B16B1_SB**16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		100	100									
Stratix		100	100									
Stratix2		50	50									
Max2		100	100									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		90	90									
Spartan2E		90	90									
Spartan3		80	80									
Virtex		90	90									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		90	90									
Virtex4		80	80									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		234										

M10_B16B1E

16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		110	110									
Stratix		110	110									
Stratix2		50	50									
Max2		110	110									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		94	92									
Spartan2E		94	92									
Spartan3		94	92									
Virtex		94	92									
Virtex2		94	92									
Virtex2p		94	92									
VirtexE		94	92									
Virtex4		94	92									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		234										

M10_B16B1E_SB**16x10-Bit Bus to 1x10-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		110	110									
Stratix		110	110									
Stratix2		50	50									
Max2		110	110									
Max3000a	40	40										
Max7000b	40	40										
Max7000ae	40	40										
Max7000s	40	40										
Spartan2		94	92									
Spartan2E		94	92									
Spartan3		94	92									
Virtex		94	92									
Virtex2		94	92									
Virtex2p		94	92									
VirtexE		94	92									
Virtex4		94	92									
CoolRunner2	10											
CoolRunnerXpla3	10											
Xc9500	10											
Xc9500XL	10											
Xc9500XV	10											
ProAsicPlus		234										

M12_B1B2

1x12-Bit Bus to 2x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		24										

M12_B1B2E**1x12-Bit Bus to 2x12-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		24	24									
Max2		24	24									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	24											
CoolRunnerXpla3	24											
Xc9500	24											
Xc9500XL	24											
Xc9500XV	24											
ProAsicPlus		26										

M12_B1B4

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		52										

M12_B1B4_SB**1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		52										

M12_B1B4E

1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		54										

M12_B1B4E_SB**1x12-Bit Bus to 4x12-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		48	48									
Max2		48	48									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	48											
CoolRunnerXpla3	48											
Xc9500	48											
Xc9500XL	48											
Xc9500XV	48											
ProAsicPlus		54										

M12_B1B8

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		96	96									
Stratix		96	96									
Stratix2		96	96									
Max2		96	96									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		96	96									
Spartan2E		96	96									
Spartan3		96	96									
Virtex		96	96									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		96	96									
Virtex4		96	96									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		108										

M12_B1B8_SB**1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		96	96									
Stratix		96	96									
Stratix2		96	96									
Max2		96	96									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		96	96									
Spartan2E		96	96									
Spartan3		96	96									
Virtex		96	96									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		96	96									
Virtex4		96	96									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		108										

M12_B1B8E

1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		104	104									
Stratix		104	104									
Stratix2		96	96									
Max2		104	104									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		98	98									
Spartan2E		98	98									
Spartan3		98	98									
Virtex		98	98									
Virtex2		98	98									
Virtex2p		98	98									
VirtexE		98	98									
Virtex4		98	98									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		110										

M12_B1B8E_SB**1x12-Bit Bus to 8x12-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		104	104									
Stratix		104	104									
Stratix2		96	96									
Max2		104	104									
Max3000a	96	96										
Max7000b	96	96										
Max7000ae	96	96										
Max7000s	96	96										
Spartan2		98	98									
Spartan2E		98	98									
Spartan3		98	98									
Virtex		98	98									
Virtex2		98	98									
Virtex2p		98	98									
VirtexE		98	98									
Virtex4		98	98									
CoolRunner2	96											
CoolRunnerXpla3	96											
Xc9500	96											
Xc9500XL	96											
Xc9500XV	96											
ProAsicPlus		110										

M12_B1B16

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		208	208									
Stratix		208	208									
Stratix2		192	192									
Max2		208	208									
Max3000a	192	192										
Max7000b	192	192										
Max7000ae	192	192										
Max7000s	192	192										
Spartan2		200	200									
Spartan2E		200	200									
Spartan3		200	200									
Virtex		200	200									
Virtex2		200	200									
Virtex2p		200	200									
VirtexE		200	200									
Virtex4		200	200									
CoolRunner2	192											
CoolRunnerXpla3	192											
ProAsicPlus		216										

M12_B1B16_SB**1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		208	208									
Stratix		208	208									
Stratix2		192	192									
Max2		208	208									
Max3000a	192	192										
Max7000b	192	192										
Max7000ae	192	192										
Max7000s	192	192										
Spartan2		202	200									
Spartan2E		202	200									
Spartan3		200	200									
Virtex		202	200									
Virtex2		200	200									
Virtex2p		200	200									
VirtexE		202	200									
Virtex4		202	200									
CoolRunner2	192											
CoolRunnerXpla3	192											
ProAsicPlus		216										

M12_B1B16E

1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		210	210									
Stratix		210	210									
Stratix2		208	208									
Max2		210	210									
Max3000a	192	192										
Max7000b	192	192										
Max7000ae	192	192										
Max7000s	192	192										
Spartan2		200	200									
Spartan2E		200	200									
Spartan3		200	200									
Virtex		200	200									
Virtex2		200	200									
Virtex2p		200	200									
VirtexE		200	200									
Virtex4		200	200									
CoolRunner2	192											
CoolRunnerXpla3	192											
ProAsicPlus		218										

M12_B1B16E_SB**1x12-Bit Bus to 16x12-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		210	210									
Stratix		210	210									
Stratix2		208	208									
Max2		210	210									
Max3000a	192	192										
Max7000b	192	192										
Max7000ae	192	192										
Max7000s	192	192										
Spartan2		200	200									
Spartan2E		200	200									
Spartan3		200	200									
Virtex		200	200									
Virtex2		200	200									
Virtex2p		200	200									
VirtexE		200	200									
Virtex4		200	200									
CoolRunner2	192											
CoolRunnerXpla3	192											
ProAsicPlus		218										

M12_B2B1

2x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		12										

M12_B2B1E

2x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		12	12									
Stratix		12	12									
Stratix2		12	12									
Max2		12	12									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		12	12									
Spartan2E		12	12									
Spartan3		12	12									
Virtex		12	12									
Virtex2		12	12									
Virtex2p		12	12									
VirtexE		12	12									
Virtex4		12	12									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		24										

M12_B4B1

4x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		12	12									
Max2		24	24									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		52										

M12_B4B1_SB

4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		24	24									
Stratix		24	24									
Stratix2		12	12									
Max2		24	24									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		24	24									
Spartan2E		24	24									
Spartan3		24	24									
Virtex		24	24									
Virtex2		24	24									
Virtex2p		24	24									
VirtexE		24	24									
Virtex4		24	24									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		52										

M12_B4B1E

4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		12	12									
Max2		36	36									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		64										

M12_B4B1E_SB**4x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		36	36									
Stratix		36	36									
Stratix2		12	12									
Max2		36	36									
Max3000a	12	12										
Max7000b	12	12										
Max7000ae	12	12										
Max7000s	12	12										
Spartan2		36	36									
Spartan2E		36	36									
Spartan3		36	36									
Virtex		36	36									
Virtex2		36	36									
Virtex2p		36	36									
VirtexE		36	36									
Virtex4		36	36									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		64										

M12_B8B1

8x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		36	36									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		132										

M12_B8B1_SB**8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		36	36									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		132										

M12_B8B1E

8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		36	36									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		60	60									
Spartan2E		60	60									
Spartan3		60	60									
Virtex		60	60									
Virtex2		60	60									
Virtex2p		60	60									
VirtexE		60	60									
Virtex4		60	60									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		144										

M12_B8B1E_SB

8x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		60	60									
Stratix		60	60									
Stratix2		36	36									
Max2		60	60									
Max3000a	24	24										
Max7000b	24	24										
Max7000ae	24	24										
Max7000s	24	24										
Spartan2		60	60									
Spartan2E		60	60									
Spartan3		60	60									
Virtex		60	60									
Virtex2		60	60									
Virtex2p		60	60									
VirtexE		60	60									
Virtex4		60	60									
CoolRunner2	12											
CoolRunnerXpla3	12											
Xc9500	12											
Xc9500XL	12											
Xc9500XV	12											
ProAsicPlus		144										

M12_B16B1

16x12-Bit Bus to 1x12-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		120	120									
Stratix		120	120									
Stratix2		60	60									
Max2		120	120									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		108	108									
Spartan2E		108	108									
Spartan3		96	96									
Virtex		108	108									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		108	108									
Virtex4		96	96									
CoolRunner2	12											
CoolRunnerXpla3	12											
ProAsicPlus		276										

M12_B16B1_SB**16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Bus Version Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		120	120									
Stratix		120	120									
Stratix2		60	60									
Max2		120	120									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		108	108									
Spartan2E		108	108									
Spartan3		96	96									
Virtex		108	108									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		108	108									
Virtex4		96	96									
CoolRunner2	12											
CoolRunnerXpla3	12											
ProAsicPlus		276										

M12_B16B1E

16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		132	132									
Stratix		132	132									
Stratix2		60	60									
Max2		132	132									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		112	110									
Spartan2E		112	110									
Spartan3		112	110									
Virtex		112	110									
Virtex2		112	110									
Virtex2p		112	110									
VirtexE		112	110									
Virtex4		112	110									
CoolRunner2	12											
CoolRunnerXpla3	12											
ProAsicPlus		276										

M12_B16B1E_SB**16x12-Bit Bus to 1x12-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		132	132									
Stratix		132	132									
Stratix2		60	60									
Max2		132	132									
Max3000a	48	48										
Max7000b	48	48										
Max7000ae	48	48										
Max7000s	48	48										
Spartan2		112	110									
Spartan2E		112	110									
Spartan3		112	110									
Virtex		112	110									
Virtex2		112	110									
Virtex2p		112	110									
VirtexE		112	110									
Virtex4		112	110									
CoolRunner2	12											
CoolRunnerXpla3	12											
ProAsicPlus		276										

M16_B1B2

1x16-Bit Bus to 2x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		32										

M16_B1B2E**1x16-Bit Bus to 2x16-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		34										

M16_B1B4

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		68										

M16_B1B4_SB**1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		68										

M16_B1B4E

1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		70										

M16_B1B4E_SB**1x16-Bit Bus to 4x16-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		70										

M16_B1B8

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		134	134									
Spartan2E		134	134									
Spartan3		134	134									
Virtex		134	134									
Virtex2		134	134									
Virtex2p		134	134									
VirtexE		134	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		140										

M16_B1B8_SB**1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		134	134									
Spartan2E		134	134									
Spartan3		134	134									
Virtex		134	134									
Virtex2		134	134									
Virtex2p		134	134									
VirtexE		134	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		140										

M16_B1B8E

1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		136	136									
Stratix		136	136									
Stratix2		128	128									
Max2		136	136									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		134	134									
Spartan2E		134	134									
Spartan3		134	134									
Virtex		134	134									
Virtex2		134	134									
Virtex2p		134	134									
VirtexE		134	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		142										

M16_B1B8E_SB**1x16-Bit Bus to 8x16-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		136	136									
Stratix		136	136									
Stratix2		128	128									
Max2		136	136									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		134	134									
Spartan2E		134	134									
Spartan3		134	134									
Virtex		134	134									
Virtex2		134	134									
Virtex2p		134	134									
VirtexE		134	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		142										

M16_B1B16

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		272	272									
Stratix		272	272									
Stratix2		256	256									
Max2		272	272									
Max3000a	256	256										
Max7000b	256	256										
Max7000ae	256	256										
Max7000s	256	256										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		280										

M16_B1B16_SB**1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		272	272									
Stratix		272	272									
Stratix2		256	256									
Max2		272	272									
Max3000a	256	256										
Max7000b	256	256										
Max7000ae	256	256										
Max7000s	256	256										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		280										

M16_B1B16E

1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		274	274									
Stratix		274	274									
Stratix2		272	272									
Max2		274	274									
Max3000a	272	272										
Max7000b	272	272										
Max7000ae	272	272										
Max7000s	272	272										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		282										

M16_B1B16E_SB**1x16-Bit Bus to 16x16-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		274	274									
Stratix		274	274									
Stratix2		272	272									
Max2		274	274									
Max3000a	272	272										
Max7000b	272	272										
Max7000ae	272	272										
Max7000s	272	272										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		282										

M16_B2B1

2x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		16										

M16_B2B1E**2x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		16	16									
Stratix		16	16									
Stratix2		16	16									
Max2		16	16									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		16	16									
Spartan2E		16	16									
Spartan3		16	16									
Virtex		16	16									
Virtex2		16	16									
Virtex2p		16	16									
VirtexE		16	16									
Virtex4		16	16									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		32										

M16_B4B1

4x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		16	16									
Max2		32	32									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		68										

M16_B4B1_SB

4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		16	16									
Max2		32	32									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		68										

M16_B4B1E

4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		16	16									
Max2		48	48									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		84										

M16_B4B1E_SB**4x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		48	48									
Stratix		48	48									
Stratix2		16	16									
Max2		48	48									
Max3000a	16	16										
Max7000b	16	16										
Max7000ae	16	16										
Max7000s	16	16										
Spartan2		48	48									
Spartan2E		48	48									
Spartan3		48	48									
Virtex		48	48									
Virtex2		48	48									
Virtex2p		48	48									
VirtexE		48	48									
Virtex4		48	48									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		84										

M16_B8B1

8x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		48	48									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		172										

M16_B8B1_SB**8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		48	48									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		172										

M16_B8B1E

8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		48	48									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		80	80									
Spartan2E		80	80									
Spartan3		80	80									
Virtex		80	80									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		80	80									
Virtex4		80	80									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		188										

M16_B8B1E_SB

8x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		80	80									
Stratix		80	80									
Stratix2		48	48									
Max2		80	80									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		80	80									
Spartan2E		80	80									
Spartan3		80	80									
Virtex		80	80									
Virtex2		80	80									
Virtex2p		80	80									
VirtexE		80	80									
Virtex4		80	80									
CoolRunner2	16											
CoolRunnerXpla3	16											
Xc9500	16											
Xc9500XL	16											
Xc9500XV	16											
ProAsicPlus		188										

M16_B16B1

16x16-Bit Bus to 1x16-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		80	80									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		146	146									
Spartan2E		146	146									
Spartan3		130	130									
Virtex		146	146									
Virtex2		130	130									
Virtex2p		130	130									
VirtexE		146	146									
Virtex4		130	130									
ProAsicPlus		360										

M16_B16B1_SB**16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		80	80									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		146	146									
Spartan2E		146	146									
Spartan3		130	130									
Virtex		146	146									
Virtex2		130	130									
Virtex2p		130	130									
VirtexE		146	146									
Virtex4		130	130									
ProAsicPlus		360										

M16_B16B1E

16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		176	176									
Stratix		176	176									
Stratix2		80	80									
Max2		176	176									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		148	146									
Spartan2E		148	146									
Spartan3		148	146									
Virtex		148	146									
Virtex2		148	146									
Virtex2p		148	146									
VirtexE		148	146									
Virtex4		148	146									
ProAsicPlus		360										

M16_B16B1E_SB**16x16-Bit Bus to 1x16-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		176	176									
Stratix		176	176									
Stratix2		80	80									
Max2		176	176									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		148	146									
Spartan2E		148	146									
Spartan3		148	146									
Virtex		148	146									
Virtex2		148	146									
Virtex2p		148	146									
VirtexE		148	146									
Virtex4		148	146									
ProAsicPlus		360										

M32_B1B2

1x32-Bit Bus to 2x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		64										

M32_B1B2E**1x32-Bit Bus to 2x32-Bit Bus Multiplexer (Demultiplex) With Enable**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		64	64									
Max2		64	64									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	64											
CoolRunnerXpla3	64											
Xc9500	64											
Xc9500XL	64											
Xc9500XV	64											
ProAsicPlus		68										

M32_B1B4

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		128	128									
Spartan2E		128	128									
Spartan3		128	128									
Virtex		128	128									
Virtex2		128	128									
Virtex2p		128	128									
VirtexE		128	128									
Virtex4		128	128									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		136										

M32_B1B4_SB**1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		128	128									
Spartan2E		128	128									
Spartan3		128	128									
Virtex		128	128									
Virtex2		128	128									
Virtex2p		128	128									
VirtexE		128	128									
Virtex4		128	128									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		136										

M32_B1B4E

1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	132	132										
Max7000b	132	132										
Max7000ae	132	132										
Max7000s	132	132										
Spartan2		136	134									
Spartan2E		136	134									
Spartan3		136	134									
Virtex		136	134									
Virtex2		136	134									
Virtex2p		136	134									
VirtexE		136	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		138										

M32_B1B4E_SB**1x32-Bit Bus to 4x32-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		128	128									
Stratix		128	128									
Stratix2		128	128									
Max2		128	128									
Max3000a	132	132										
Max7000b	132	132										
Max7000ae	132	132										
Max7000s	132	132										
Spartan2		136	134									
Spartan2E		136	134									
Spartan3		136	134									
Virtex		136	134									
Virtex2		136	134									
Virtex2p		136	134									
VirtexE		136	134									
Virtex4		134	134									
CoolRunner2	128											
CoolRunnerXpla3	128											
Xc9500	128											
Xc9500XL	128											
Xc9500XV	128											
ProAsicPlus		138										

M32_B1B8

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		256	256									
Stratix		256	256									
Stratix2		256	256									
Max2		256	256									
Max3000a	256	256										
Max7000b	256	256										
Max7000ae	256	256										
Max7000s	256	256										
Spartan2		268	265									
Spartan2E		268	265									
Spartan3		268	265									
Virtex		268	265									
Virtex2		268	265									
Virtex2p		268	265									
VirtexE		268	265									
Virtex4		268	265									
ProAsicPlus		276										

M32_B1B8_SB**1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		256	256									
Stratix		256	256									
Stratix2		256	256									
Max2		256	256									
Max3000a	256	256										
Max7000b	256	256										
Max7000ae	256	256										
Max7000s	256	256										
Spartan2		268	265									
Spartan2E		268	265									
Spartan3		268	265									
Virtex		268	265									
Virtex2		268	265									
Virtex2p		268	265									
VirtexE		268	265									
Virtex4		268	265									
ProAsicPlus		276										

M32_B1B8E

1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		264	264									
Stratix		264	264									
Stratix2		256	256									
Max2		264	264									
Max3000a	264	264										
Max7000b	264	264										
Max7000ae	264	264										
Max7000s	264	264										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		278										

M32_B1B8E_SB**1x32-Bit Bus to 8x32-Bit Bus Multiplexer (Demultiplex) With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		264	264									
Stratix		264	264									
Stratix2		256	256									
Max2		264	264									
Max3000a	264	264										
Max7000b	264	264										
Max7000ae	264	264										
Max7000s	264	264										
Spartan2		268	266									
Spartan2E		268	266									
Spartan3		268	266									
Virtex		268	266									
Virtex2		268	266									
Virtex2p		268	266									
VirtexE		268	266									
Virtex4		268	266									
ProAsicPlus		278										

M32_B1B16

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex)

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		528	528									
Stratix		528	528									
Stratix2		513	512									
Max2		528	528									
Max3000a	512	512										
Max7000b	512	512										
Max7000ae	512	512										
Max7000s	512	512										
Spartan2		534	532									
Spartan2E		534	532									
Spartan3		534	532									
Virtex		534	532									
Virtex2		534	532									
Virtex2p		534	532									
VirtexE		534	532									
Virtex4		534	532									
ProAsicPlus		552										

M32_B1B16_SB**1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		528	528									
Stratix		528	528									
Stratix2		513	512									
Max2		528	528									
Max3000a	512	512										
Max7000b	512	512										
Max7000ae	512	512										
Max7000s	512	512										
Spartan2		534	532									
Spartan2E		534	532									
Spartan3		534	532									
Virtex		534	532									
Virtex2		534	532									
Virtex2p		534	532									
VirtexE		534	532									
Virtex4		534	532									
ProAsicPlus		552										

M32_B1B16E

1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		530	530									
Stratix		530	530									
Stratix2		529	528									
Max2		530	530									
Max3000a	528	528										
Max7000b	528	528										
Max7000ae	528	528										
Max7000s	528	528										
Spartan2		534	532									
Spartan2E		534	532									
Spartan3		532	529									
Virtex		534	532									
Virtex2		532	529									
Virtex2p		532	529									
VirtexE		534	532									
Virtex4		532	529									
ProAsicPlus		554										

M32_B1B16E_SB**1x32-Bit Bus to 16x32-Bit Bus Multiplexer (Demultiplex) With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		530	530									
Stratix		530	530									
Stratix2		529	528									
Max2		530	530									
Max3000a	528	528										
Max7000b	528	528										
Max7000ae	528	528										
Max7000s	528	528										
Spartan2		532	532									
Spartan2E		532	532									
Spartan3		532	532									
Virtex		532	532									
Virtex2		532	532									
Virtex2p		532	532									
VirtexE		532	532									
Virtex4		532	532									
ProAsicPlus		554										

M32_B2B1

2x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		32										

M32_B2B1E

2x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		32	32									
Stratix		32	32									
Stratix2		32	32									
Max2		32	32									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		32	32									
Spartan2E		32	32									
Spartan3		32	32									
Virtex		32	32									
Virtex2		32	32									
Virtex2p		32	32									
VirtexE		32	32									
Virtex4		32	32									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		88										

M32_B4B1

4x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		32	32									
Max2		64	64									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		136										

M32_B4B1_SB**4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		64	64									
Stratix		64	64									
Stratix2		32	32									
Max2		64	64									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		64	64									
Spartan2E		64	64									
Spartan3		64	64									
Virtex		64	64									
Virtex2		64	64									
Virtex2p		64	64									
VirtexE		64	64									
Virtex4		64	64									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		136										

M32_B4B1E

4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		96	96									
Stratix		96	96									
Stratix2		32	32									
Max2		96	96									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		96	96									
Spartan2E		96	96									
Spartan3		96	96									
Virtex		96	96									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		96	96									
Virtex4		96	96									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		168										

M32_B4B1E_SB**4x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		96	96									
Stratix		96	96									
Stratix2		32	32									
Max2		96	96									
Max3000a	32	32										
Max7000b	32	32										
Max7000ae	32	32										
Max7000s	32	32										
Spartan2		96	96									
Spartan2E		96	96									
Spartan3		96	96									
Virtex		96	96									
Virtex2		96	96									
Virtex2p		96	96									
VirtexE		96	96									
Virtex4		96	96									
CoolRunner2	32											
CoolRunnerXpla3	32											
Xc9500	32											
Xc9500XL	32											
Xc9500XV	32											
ProAsicPlus		168										

M32_B8B1**8x32-Bit Bus to 1x32-Bit Bus Multiplexer**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		96	96									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		130	130									
Spartan2E		130	130									
Spartan3		130	130									
Virtex		130	130									
Virtex2		130	130									
Virtex2p		130	130									
VirtexE		130	130									
Virtex4		130	130									
ProAsicPlus		340										

M32_B8B1_SB

8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		96	96									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		130	130									
Spartan2E		130	130									
Spartan3		130	130									
Virtex		130	130									
Virtex2		130	130									
Virtex2p		130	130									
VirtexE		130	130									
Virtex4		130	130									
ProAsicPlus		340										

M32_B8B1E

8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		96	96									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		162	162									
Spartan2E		162	162									
Spartan3		162	162									
Virtex		162	162									
Virtex2		162	162									
Virtex2p		162	162									
VirtexE		162	162									
Virtex4		162	162									
ProAsicPlus		372										

M32_B8B1E_SB**8x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		160	160									
Stratix		160	160									
Stratix2		96	96									
Max2		160	160									
Max3000a	64	64										
Max7000b	64	64										
Max7000ae	64	64										
Max7000s	64	64										
Spartan2		162	162									
Spartan2E		162	162									
Spartan3		162	162									
Virtex		162	162									
Virtex2		162	162									
Virtex2p		162	162									
VirtexE		162	162									
Virtex4		162	162									
ProAsicPlus		372										

M32_B16B1

16x32-Bit Bus to 1x32-Bit Bus Multiplexer

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		320	320									
Stratix		320	320									
Stratix2		160	160									
Max2		320	320									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		292	291									
Spartan2E		292	291									
Spartan3		260	259									
Virtex		292	291									
Virtex2		260	259									
Virtex2p		260	259									
VirtexE		292	291									
Virtex4		260	259									
ProAsicPlus		712										

M32_B16B1_SB**16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		320	320									
Stratix		320	320									
Stratix2		160	160									
Max2		320	320									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		292	291									
Spartan2E		292	291									
Spartan3		260	259									
Virtex		292	291									
Virtex2		260	259									
Virtex2p		260	259									
VirtexE		292	291									
Virtex4		260	259									
ProAsicPlus		712										

M32_B16B1E

16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		352	352									
Stratix		352	352									
Stratix2		160	160									
Max2		352	352									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		294	292									
Spartan2E		294	292									
Spartan3		294	292									
Virtex		294	292									
Virtex2		294	292									
Virtex2p		294	292									
VirtexE		294	292									
Virtex4		294	292									
ProAsicPlus		712										

M32_B16B1E_SB**16x32-Bit Bus to 1x32-Bit Bus Multiplexer With Enable and Bus Version Select**

Device Family	Macrocells	Logic Cells/Elements	4-Input LUT	Registers	Block RAM (16K-Bit)	Block RAM (4K-Bit)	Block RAM (256x9)	Block Multipliers	Total PLLs	Total DLLs	D/GCLK	DCMs
Cyclone		352	352									
Stratix		352	352									
Stratix2		160	160									
Max2		352	352									
Max3000a	128	128										
Max7000b	128	128										
Max7000ae	128	128										
Max7000s	128	128										
Spartan2		294	292									
Spartan2E		294	292									
Spartan3		294	292									
Virtex		294	292									
Virtex2		294	292									
Virtex2p		294	292									
VirtexE		294	292									
Virtex4		294	292									
ProAsicPlus		712										

Revision History

Date	Version No.	Revision
6-Dec-2004	1.0	Service pack 2 release
12-Apr-2005	1.01	Added Virtex4 and Stratix2 resource usage
16-May-2005	1.02	Regenerated resource usage statistics to reflect the corrections to De-Multiplexers
6-Jun-2005	1.03	Added MAX2 resource usage

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