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FPGA design without specialist VHDL/Verilog skills

Learn how to embed test instruments into your PCB designs

SYDNEY, Australia – August 19, 2009 – Altium is hosting a free webinar demonstrating how electronics designers, with no prior experience, can develop and test sophisticated systems using FPGAs. The [webinar](#) takes place on September 22, 2009, 2pm EDT, 11am PDT, 6pm GMT.

Marty Hauff, Master Electronics Designer Success Manager at Altium, will teach electronics designers how to embed virtual test instruments and custom interface panels in FPGAs to assist with product development, system test and field diagnosis. It will be a practical workshop demonstrating how simple it is to develop intelligent, cost-effective field and test applications using an FPGA.

“FPGAs have traditionally been associated with the specialized VHDL or Verilog languages. But this webinar demonstrates that you can design with FPGAs without having to learn any new skills. Using Altium Designer and JTAG adapter, electronics designers can immediately gain insight into the FPGA to: test, debug and gain greater control over their designs. This webinar is about how designers can easily exploit the flexibility of FPGAs for more functionality and greater hardware opportunities,” said Marty Hauff.

The first 500 registrants to view the complete webinar on September 22, 2009 will also receive a free 2GB flash drive.

For more information, or to register, visit:

<http://www.pcbshows.com/webinars/events/altium/fpga.shtml>

ENDS

About Altium

Altium Limited (ASX:ALU) provides next generation electronics design software. Altium's unified electronics design environment links all aspects of electronics product design into one process, in a single application. This helps electronics designers harness the latest devices and technologies, manage their projects across broad design 'ecosystems', and create connected, intelligent designs. For more information, visit www.altium.com.

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