



AltiumLive 2018 University Day

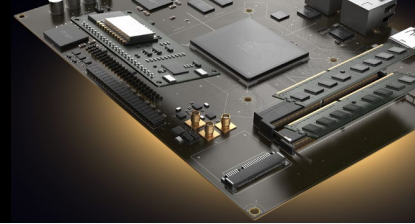
Instructor
Charley Yap
Field Application Engineer

Altium



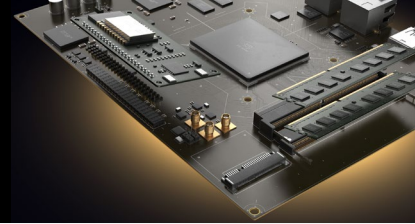
ALTIUM
DESIGNER

Impedance-Controlled Routing and Routing
Automation

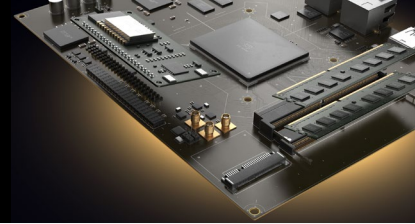


Impedance-Controlled Routing and Routing Automation

Illustrates why impedance-controlled routing is necessary
for high-speed designs.



Agenda



Controlled Impedance Routing

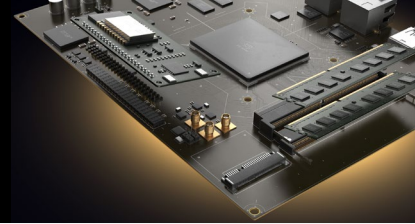
- Do we need it?
- What are the challenges?
 - When a device that switched state too quickly that the transition is complete before the signal can travel along the route.
 - Signal with fast edge can radiate and couple with adjacent routes.

Digital Design Engineer

Signal can get reflected back to the source pin. Causing the original signal to degrade or even destroy the signal data.

Can become an Electromagnetic Interference.

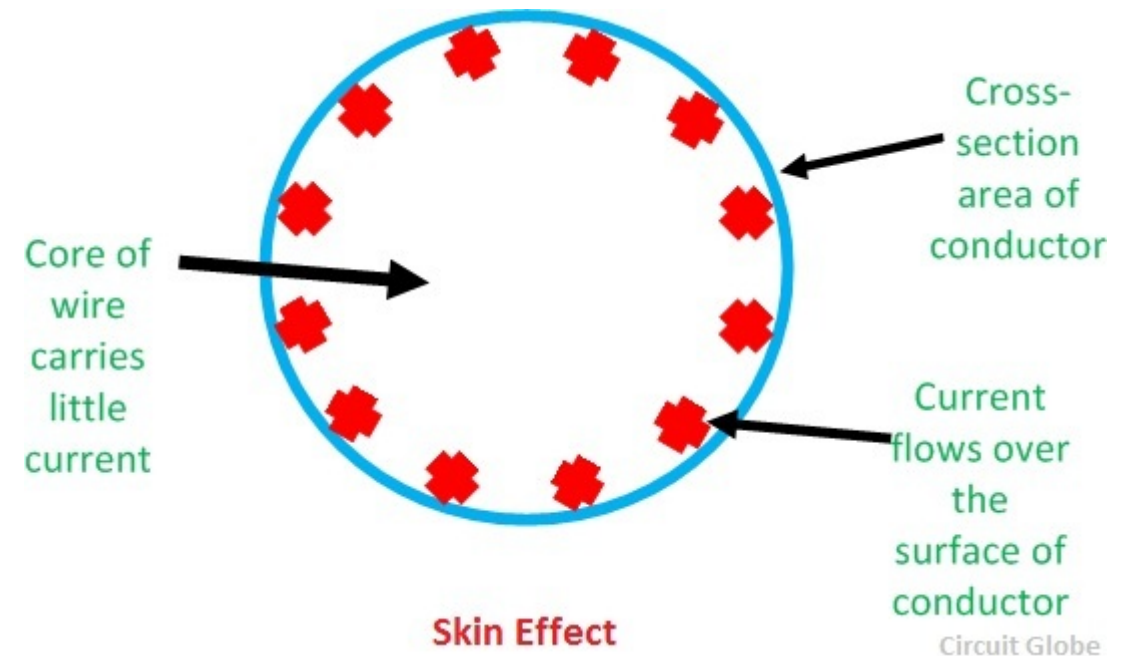


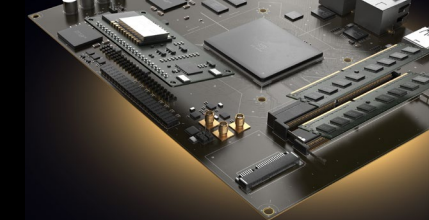


Controlled Impedance Routing

➤ Skin Effect

- When energy is distributed within a conductor such that the current density is largest near the surface of the conductor.
- Since the energy is not travelling through the conductor itself, this can slow down the signal.





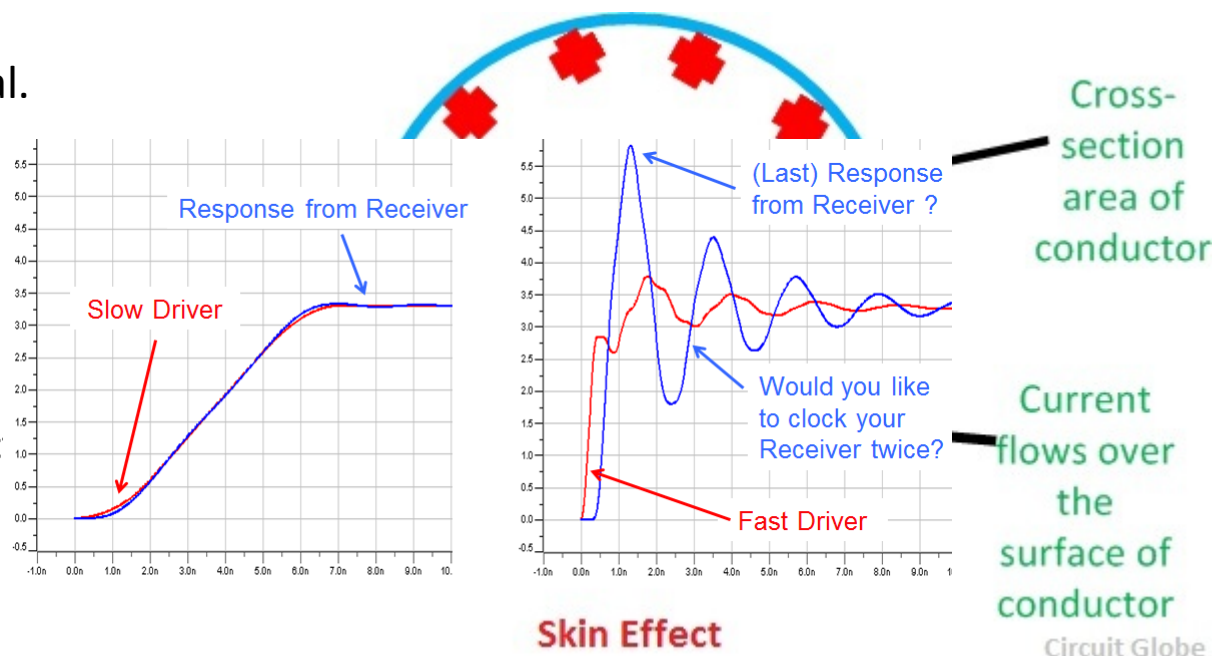
Controlled Impedance Routing

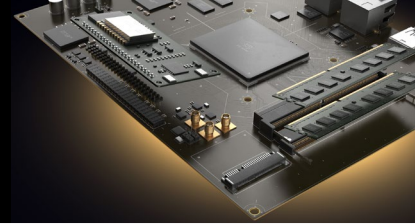
➤ Skin Effect

- When energy is distributed within a conductor such that the current density is largest near the surface of the conductor.
- Since the energy is not travelling through the conductor itself, this can slow down the signal.

➤ How can this be a problem?

- When a signal arrives at the target pin, but causes some signal to be reflected back.
- When an edge transition is completed before reflected energy arrives back, this will interact and change the original signal.

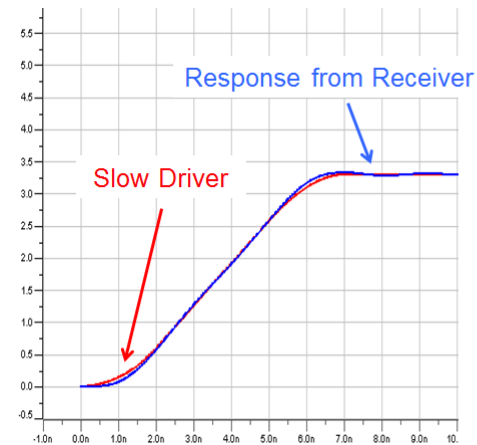


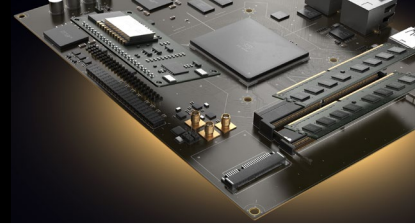


Controlled Impedance Routing

➤ Effects of reflection

- Distortions of the signal can trigger an overshoot that causes the chip to burn or undershoot that causes the device to trigger twice.





Controlled Impedance Routing

➤ Possible solutions

- Adapt a series impedance of a transmission line embedded in your PCB.
- For more complex design, a termination can be added to the signal, thus preventing the designer from using wide traces.

Characteristic Impedance Driven Width

Where The Object Matches

Net Class:

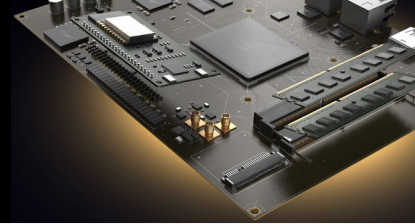
Constraints

Preferred Impedance: 50.00ohms

Min Impedance: 45.00ohms Max Impedance: 55.00ohms

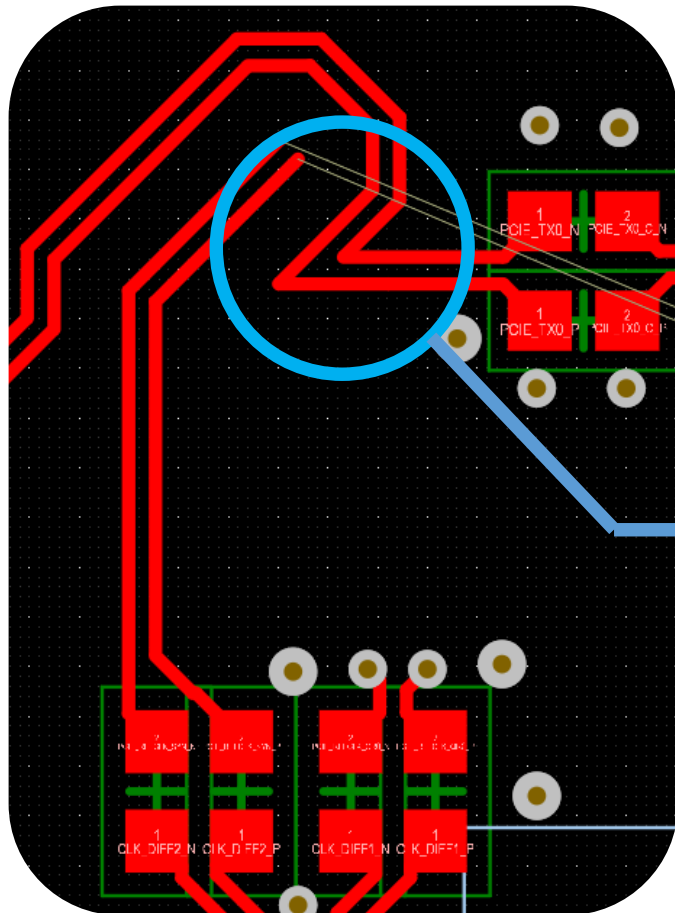
- Check Tracks/Arcs Min/Max Width Individually
- Check Min/Max Width for Physically Connected Copper (tracks, arcs, fills, pads & vias)
- Characteristic Impedance Driven Width**
- Layers in layerstack only

Attributes on Layer			Layer Stack Reference		Absolute Layer	
Min Width	Preferred Size	Max Width	Name	In	Name	In
0.178mm	0.207mm	0.241mm	Cu1 (TL)	32	TopLayer	1
0.102mm	0.13mm	0.162mm	Cu3 (Sig)	33	MidLayer1	2
0.373mm	0.434mm	0.504mm	Cu5 (Sig)	34	MidLayer2	3
0.851mm	0.982mm	1.132mm	Cu6(BL)	35	BottomLayer	32

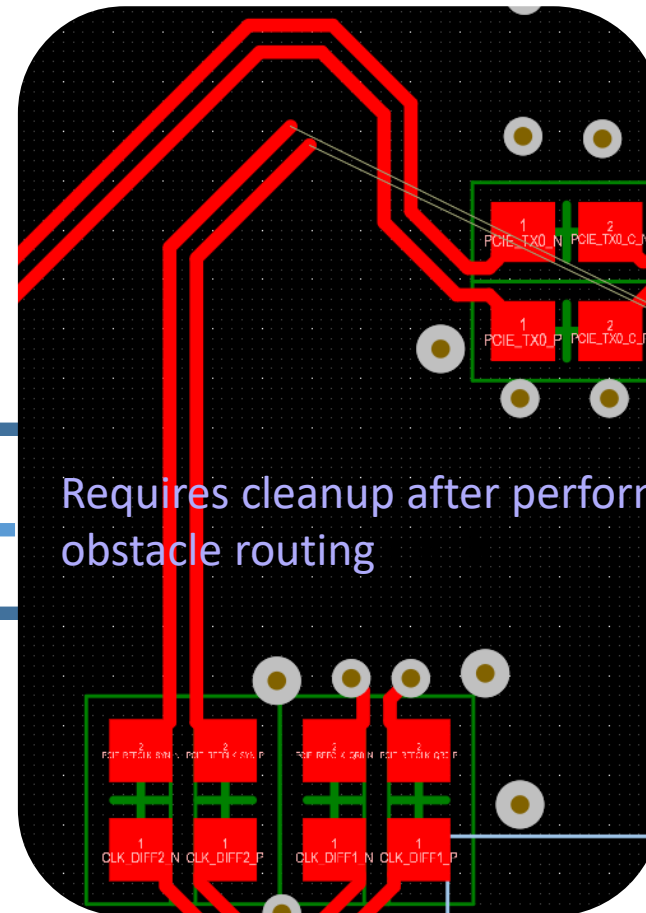


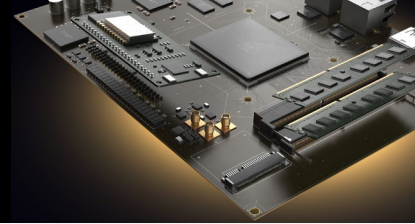
Glossing Pushed Routes

AD18



AD19





Follow Mode

- Rather than requiring the designer to route "against" the contour using careful and accurate mouse movements and click actions to ensure the new route hugs the contour, in Follow mode the designer simply clicks to nominate the contour, then moves the cursor along the contour to define the route direction.
- In Follow mode, the interactive router will add track and arc segments so that the new route follows the contour, in compliance with applicable design rules. This feature is particularly useful when placing curved routes.

DEMO

