Media Release

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University of Tokyo chooses Altium Designer for satellite programme

Altium Designer to launch the next generation of electronics designers

Sydney Australia – January 17, 2008 – Altium Limited, the leading developer of unified electronic product design solutions, has secured a deal with the University of Tokyo's Intelligent Space Systems Laboratory (ISSL) to become the electronics design platform for its satellite development programme.

ISSL has recently installed five Altium Designer licenses for use in its student satellite projects. The aim of the programme is to lower the cost and development time of space research by creating a range of sophisticated nanosatellites (satellites smaller than 10kg) for use in high performance missions.

Students at ISSL will use Altium Designer's unified platform, which combines hardware, software and programmable hardware, to develop the onboard computers and subsystem interfaces found within their various satellite designs.

"With Altium Designer we can reduce the student workload of developing electronics, including FPGAs. The simulation tools will be very useful for the evaluation of the designed circuits, reducing the possibility of mistakes," commented Prof. Shinichi Nakasuka, Director of ISSL. "Altium Designer will reduce development times and costs for nano-

satellites and deliver the best design environment for students at various levels of expertise".

ISSL is currently completing its third and fourth satellites. The third, PRISM, is a 5kg remote sensing satellite with enhanced bus functions and a refraction-based optical system. The fourth, "Nano-JASMINE," is an astrometry satellite that can create precise 3D star maps and was developed in collaboration with the National Astronomical Observatory of Japan.

Altium continues to play an important role in the development of the next generation of electronics engineers, with a particularly strong presence in university space programs. Universities such as the Massachusetts Institute of Technology, University of Toronto, Aachen University and Tokyo Institute of Technology all use Altium Designer to create the next generation of space system engineers, while over 900 universities in total employ Altium as their software tool of choice.

ENDS

About Altium
Altium Limited (ASX:ALU) is the leading developer of electronic product development solutions dedicated to unifying the different design disciplines involved in electronics product development. Altium products ensure all electronic engineers, designers, developers, and their organizations, take maximum advantage of emerging design technologies to bring smarter products to market faster and easier. Founded in 1985, Altium has headquarters in Sydney, Australia, sales offices in the United States, Europe, Japan, China, and resellers in all other major markets. For more information, please visit www.altium.com.

About Altium's Desktop NanoBoard
Altium's Desktop NanoBoard is the next generation of the industry's first development board based on FPGAs. It allows rapid and interactive implementation and debugging of electronics designs. The Desktop NanoBoard has been specially designed to take full advantage of Altium Designer's unified electronic product development system and transforms engineers' desktops into a complete and interactive electronics product development laboratory. Altium's NanoBoard architecture is unique in that it features interchangeable peripheral boards. Target programmable devices are housed on plug-in FPGA daughter boards. Engineers can easily change the target project architecture and provides a versatile reconfigurable development platform independent of the choice of FPGAs.

About Altium Designer

Altium Designer is the electronics design industry's only electronics product development system that removes the barriers imposed by disparate design flows and unifies the different design disciplines involved in electronics product development – the design of the hardware, the programmable hardware and the embedded software. Altium Designer's unified design environment means users can harness the potential of the latest electronics technologies, and move to a 'soft' design methodology without the need to acquire specialist programmable device expertise. This provides companies with increased design flexibility, reduced production costs and quickens time to market. Altium Designer also delivers the freedom to move between any device, from any vendor, at any time. It lowers total cost of ownership by eliminating the need to integrate extra devices at extra cost to increase functionality, or to create a complete solution. For more information, please visit http://www.altium.com/Products/AltiumDesigner/.

About ISSL, University of Tokyo

ISSL has been focusing on nano-satellite development with the weight range of 1-20kg for 8 years. Its primary objective is space engineering education for university level students, allowing them to experience the whole cycle of space development from mission conceptualization, satellite design to operation, all within a short period of time. Since their first two satellites were successfully launched, they are now beginning to realize their second purpose, to create a range of low cost satellites. The aim of the project is to open new space utilization fields. Their third and latest satellite PRISM is planned to be launched as a piggy-back payload of the Japanese H-IIA rocket in the summer of 2008.

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